

Model Name: GA-Z77-D3H

1.1

SHEET

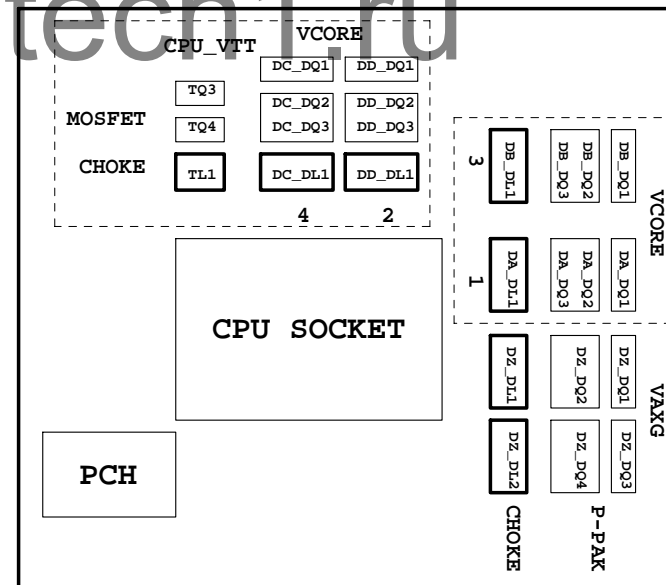
TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1155-A
05	CPU_LGA1155-B
06	CPU_LGA1155-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCIEX1*3 , PCIEX4 SLOT
16	ITE8892 PCI BRIDGE
17	PCI SLOT 1&2
18	I/O ITE8728
19	COM, -PROHOT, R_USB
20	Dual BIOS , TPM SLB9635TT
21	VT2021 CODEC
22	REAR AUDIO JACK
23	VCORE PWM_IR3564
24	VCORE PWM DRIVER IR3598
25	NCP3933 OVER VOLTAGE
26	DISCRETE POWER
27	DDR_15V & CPU_VTT PWM IR3570

SHEET

TITLE

28	DDR_15V & CPU_VTT PWM DRIVER CHL8550
29	VCCSA POWER
30	F_PANEL , F_USB2.0/3.0
31	ATX POWER, CLOCK GEN
32	HWM , KB/MS , FAN CTRL
33	LAN ATHEROS AR8151
34	N/A
35	M-SATA
36	DVI
37	HDMI , R_USB30
38	TABLE LIST
39	
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Cover Sheet		
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GA-Z77-D3H

Component value change history

Data	Change Item	Reason
0.1-1124	E-BOM	
02-1216	1. ADD PCH_HS & MOS_HS料號	
	2. PCIE gen2 switch PI3PCIE2415ZHE --> ASM1440	
	3. load-line DAR5=12K , DAR40=1.78K	
10A-0105	1. Z77料號更新	
	2. PWM Driver power vcc or +12V?	
	3. DART2 --> 47K/1/4/S , DAR44 --> 0 ohm	
10B-0113	1. Vcore & VAXG VSEN modify , DAR1,DAR51=100/4/1,DAR2,DAR54=0/4,DAC1,DAC24=3.3nF	
	2. 1.54K加替料:10RC4-001541-22R TA-I	
	1. Remove IR PWM 1X3 pin	
10C-0117	1. DA_DR11,DC_DR11,DZ_DR18 1ohm --> 0ohm	
10D-0119	1. Prochot R65 : 1.65K/4/1 --> 2.74K/4/1	
10E-EVT-0201	1. Modify choke=0.36uH , DRIVER=5V	
10F	1. IR3564要改用新料號03R	
	2. poochot change 100K	
10T	1. 0 OHM Short-pad	
	2. DDR3 FOR OC 2400MHz UP	
10G-1.01	0. PCB Rev1.01 --> ReV1.01 (DDR3 OC 2400MHz+)	
	1. RS_PWM相關線路移除 (若有上prochot pull up改100 ohm)	
	2. Add M/B ID for DDR3 OC	
	3. 固態電容區分100uF/6.3V & 100uF/16V	
10H-1.02	1. PCB Rev1.01 --> ReV1.02 (DDR3 OC 2800MHz+)	
	2. Add M/B ID for DDR3 OC	
	3. ADD DC79 FOR A_CPUPWROK	
	4. 100u 16V-->6.3V	
10I-0430	1. PWM IR3564 --> IR3564A	
	2. Remove DAESD1	
	3. RJK0393DPA 10IF9-040393-01R --> 10IF9-040393-11R	

Circuit or PCB layout change

DATE	Change Item	Reason
P67X-UD3-B3		
2011/02/18-0.1	1. 移除LAR11 ,LAR14 , NR28 ,新增NTP11	
2011/02/18-1.0	2. 新增DR388,DR389,DR391 ; Remove DQ49,DR347,DR371 3. CR44改成R0603-RH 4. R1,LAR3,RBR20,LABC25 -->R0402-2-SHORT 5. RAQ1 --> Q_TO223-MASK 6. RARN1 --> R8P4R-0402-SHORT 7. CESD1-5 --> SSOP5 8. RAQ2,RAEC1一起往下移40mil 9. CESD2文字面要標pin1	
2011/03/8-1.01	1. Add "Dolby" logo	
2011/03/8-1.02	1. UAFB1,UAFB2,UBF1,UBF2 Footprint update 1206-->1812 2. Add "AD1" FOR 5VSB	
Z68XP-D3		
1.0	1. update MINI_PCIE footprint 2. 文字面 : SLOT部分全對齊	
Z77-D3H-0.1	EVT	
0.2-1216	1. Remove SE9172 , Add VCC8 內層(注意其他內層power,跨切割) 2. SPDIF AGND --> GND 3. PCI SLOT & PCIEX1/X4 CAP COST DOWN 4. 0 ohm --> SHORT PAD 5. REMOVE SMBUS FROM COMP TO SOLDER SIDE IN DR POWER 6. SATA3 connect Change to 90 degree (記得SATA3訊號部分要做挖空) 7. Add "108dB"文字面 8. Remove VCC1_05_PCH & VCC1_8_PCH gate net 9. Add EJ168 R_USB30_1 & F_USB3 10. UAE1/UAE2 NET SWAP 11. 內層+12V要打VIA在COMA處 12. SPDIFO_HDMI走12mil	
1.0	1. SATA2-SATA3文字面要隱藏 2. DART2 移至 DC_DQ1左上方 3. Q7 & DAR31 NET Change	
1.01	1. 0 OHM SHORT PAD (LAN & AUDIO) 2. DDR3 2400MHz OC modify (DDR3 DQ 走T型)	
1.02	1. DDR3 2400MHz OC modify (縮小DDR3間距)	
1.1	1. F.B "FB0603-RH" change to "FB0402-RH" 2. ATX_12V_2X2 change to ATX_12V_2X4 3. ARS105 LAN ARS101 CO-LAYOUT 4. Add pwok R200,BC9 放在ATX 端 5. msATA LAYOUT 龍華& FOXCONN CO-LAY(變更FOOTPRINT) 6. For USB3.0 eTron EJ168A 0.11um modify (UBU1 pin88/89) 7. add VCC1_05_PCH over voltage control	

Size Custom

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Title

Document Number

Rev 1.1

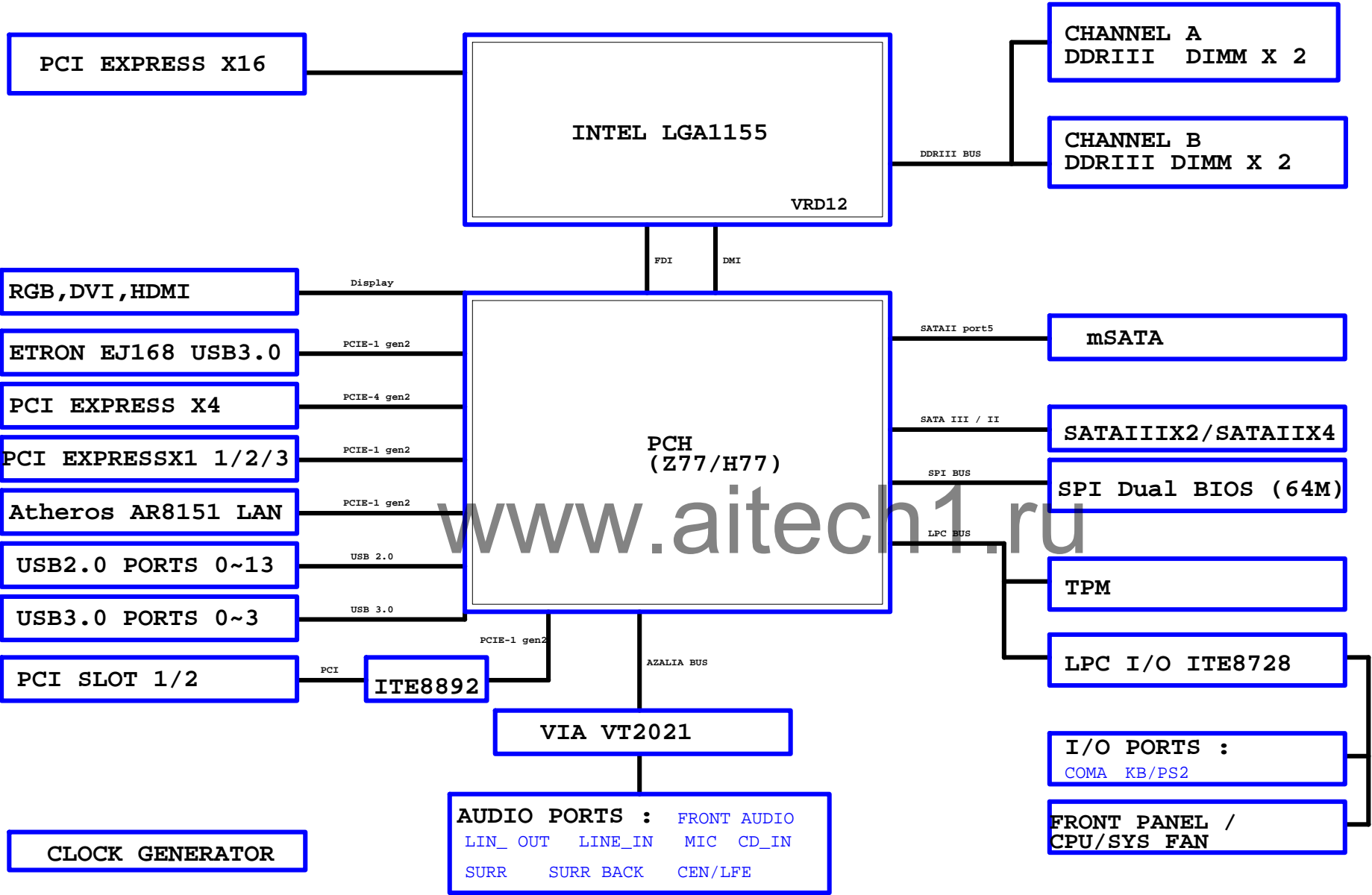
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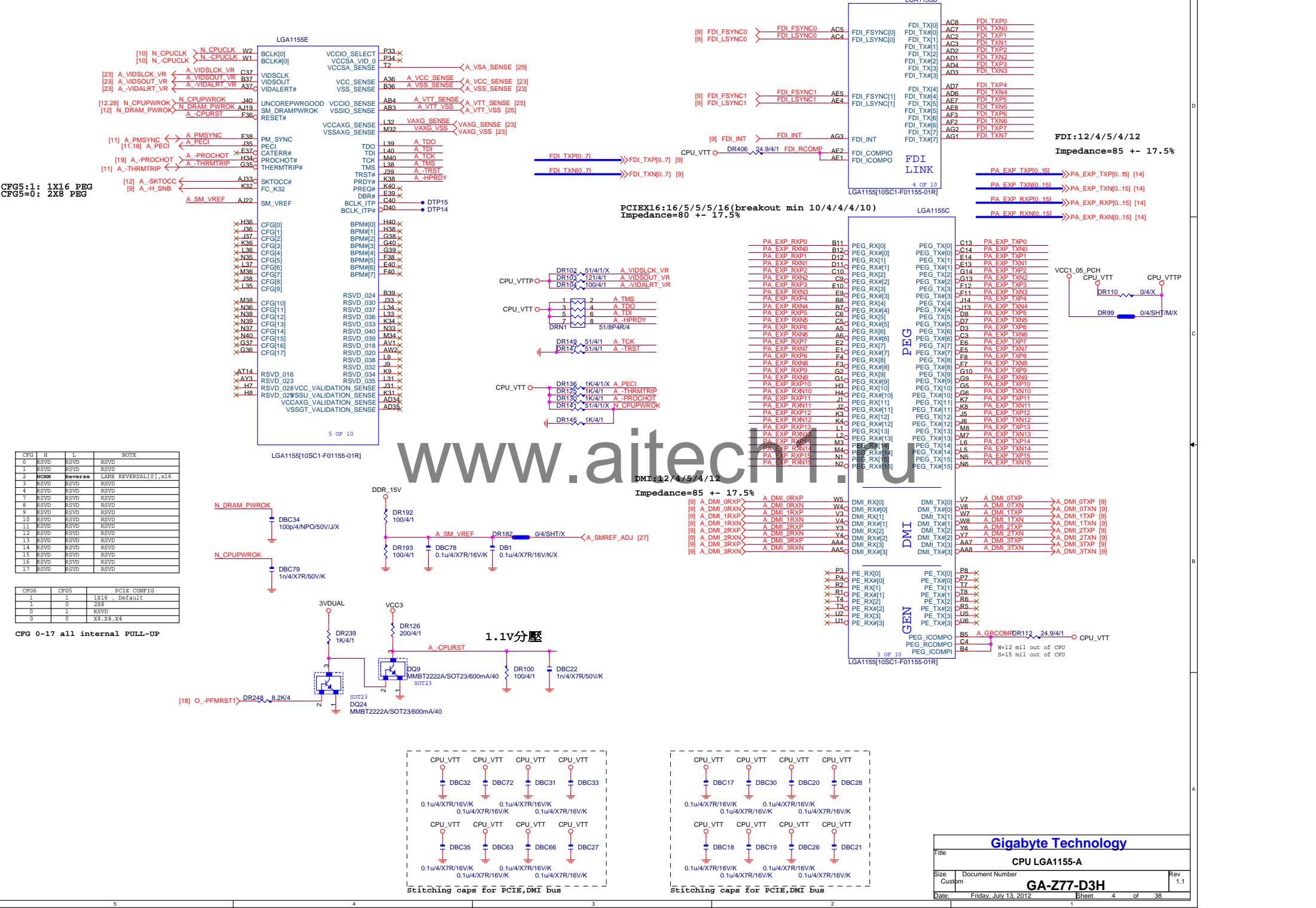
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BOM & PCB MODIFY HISTORY

GA-Z77-D3H

BLOCK DIAGRAM



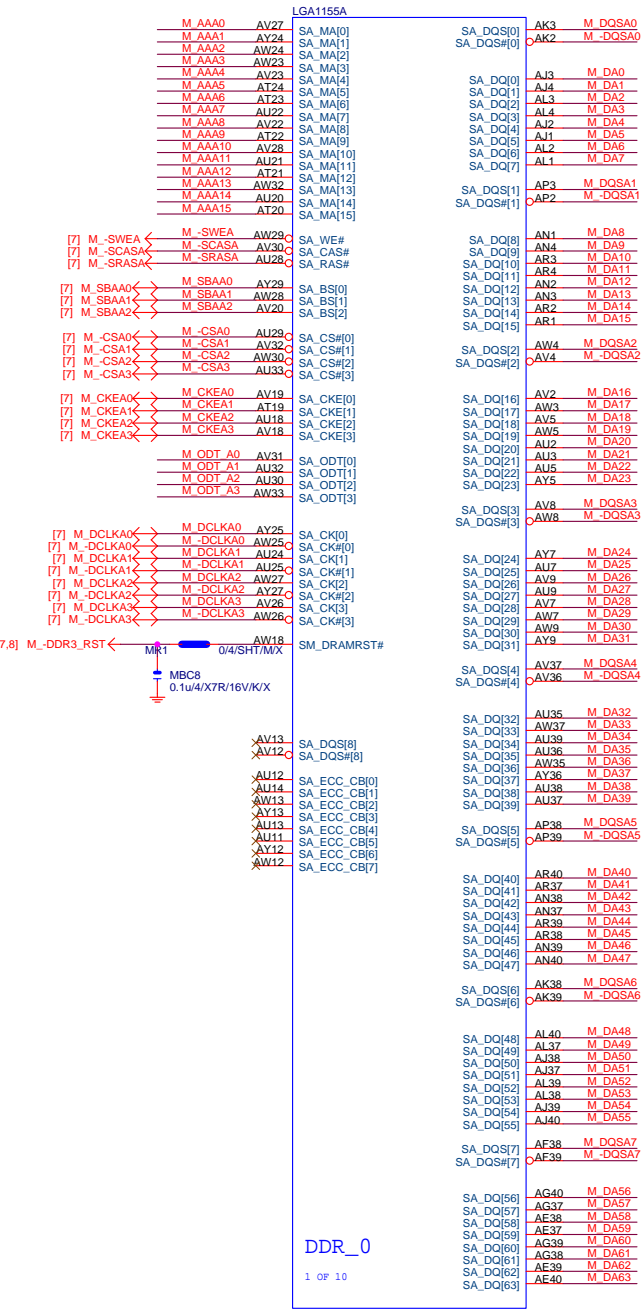


CFG5:1: 1X16 PEG
CFG5:0: 2X8 PEG

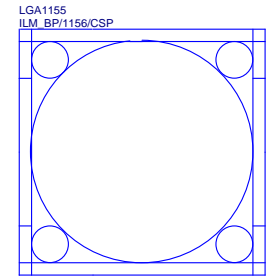
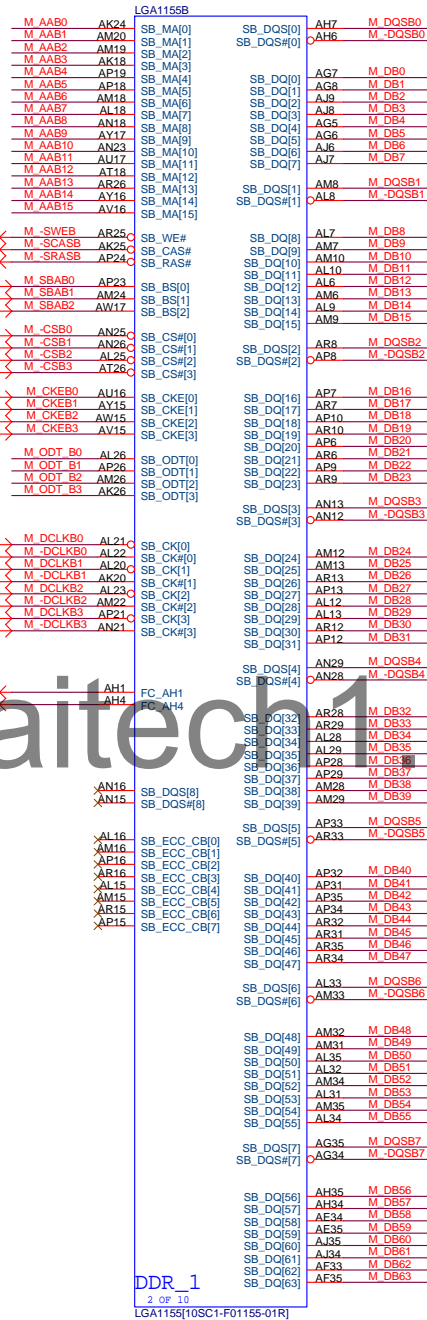
CFG	H	L	NOTE
0	RSVD	RSVD	RSVD
1	RSVD	RSVD	RSVD
2	Reverse	Reverse	LAIB REVERSAL[0],x16
3	RSVD	RSVD	RSVD
4	RSVD	RSVD	RSVD
7	RSVD	RSVD	RSVD
8	RSVD	RSVD	RSVD
9	RSVD	RSVD	RSVD
10	RSVD	RSVD	RSVD
11	RSVD	RSVD	RSVD
12	RSVD	RSVD	RSVD
13	RSVD	RSVD	RSVD
14	RSVD	RSVD	RSVD
15	RSVD	RSVD	RSVD
16	RSVD	RSVD	RSVD
17	RSVD	RSVD	RSVD

CFG6	CFG5	PCIE CONFIG
1	1	1X16, Default
1	0	2X8
0	1	RSVD
0	0	X8,X4,X4

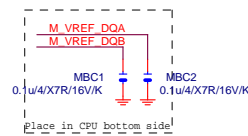
CFG 0-17 all internal PULL-UP



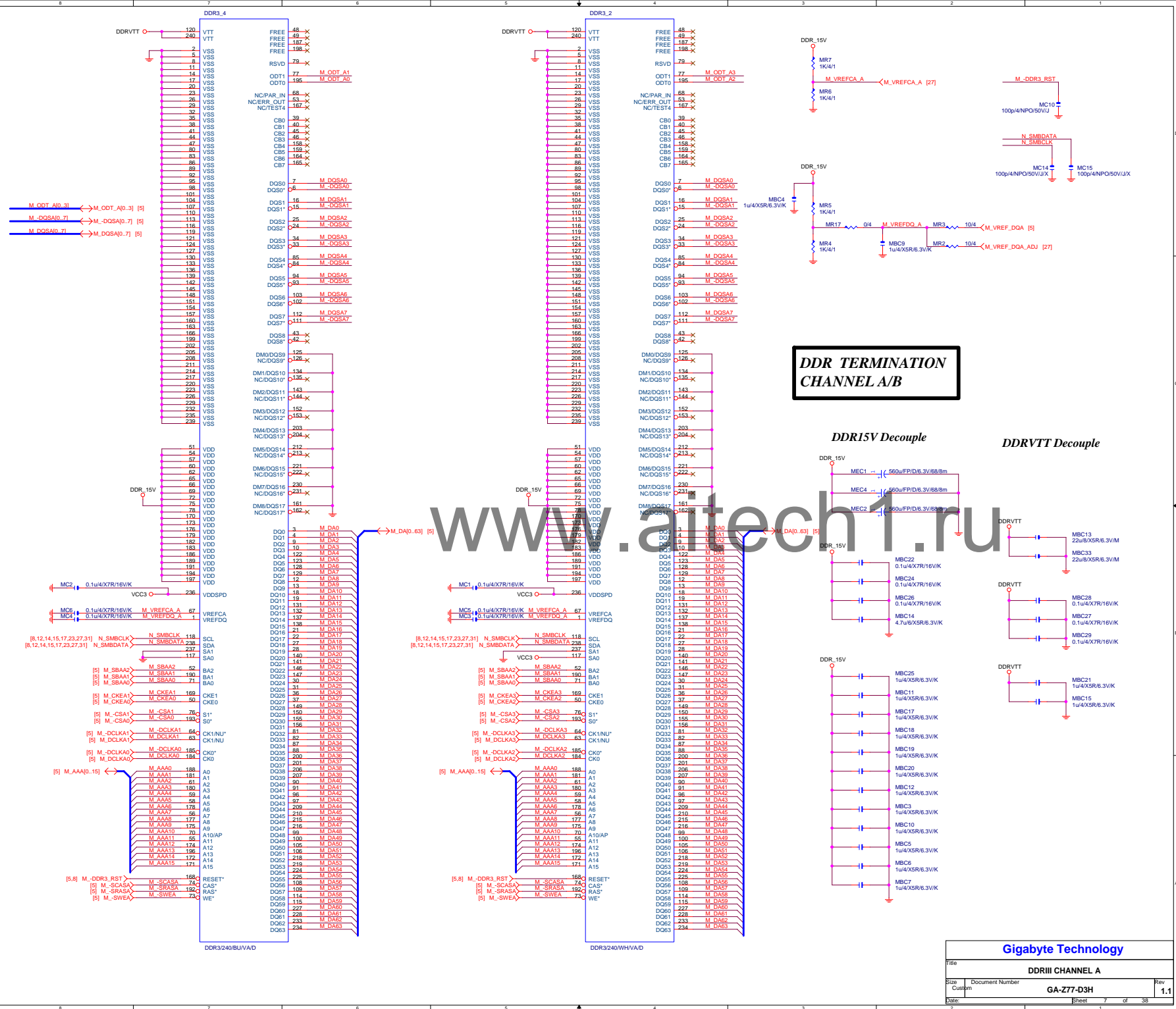
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Need check the new CPU ME

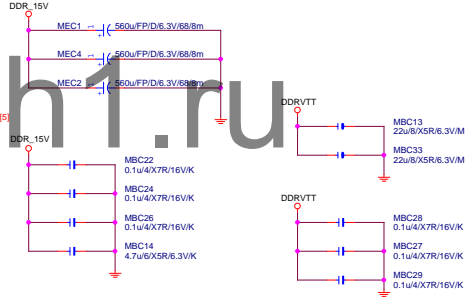


Gigabyte Technology		
CPU LGA1156-B		
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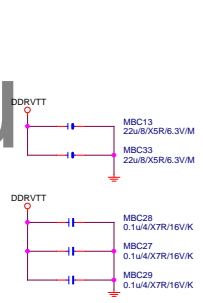


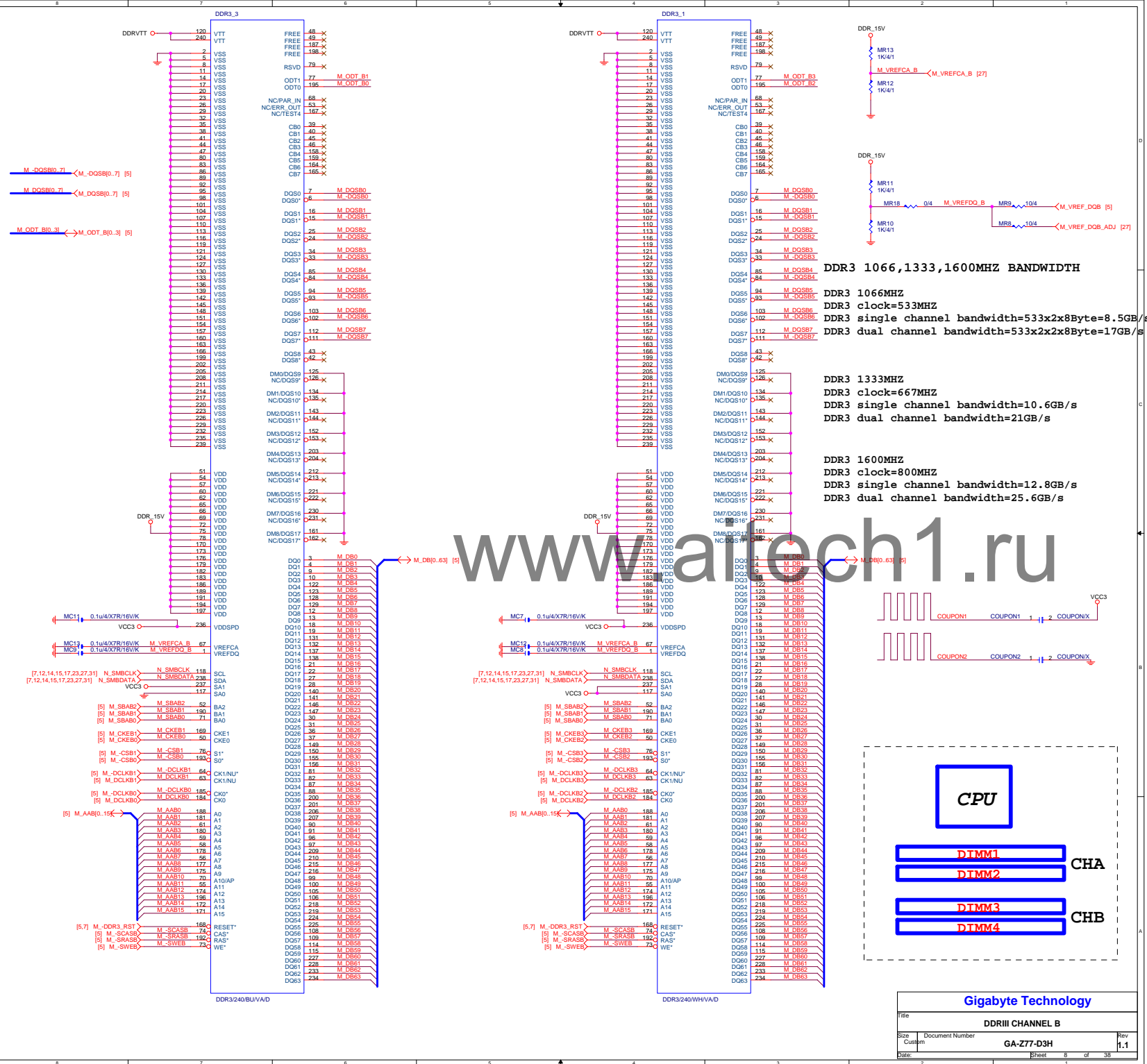
DDR TERMINATION
CHANNEL A/B

DDR15V Decouple



DDRVRTT Decouple



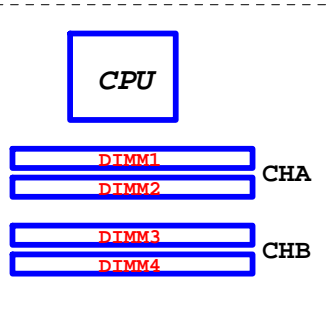


DDR3 1066,1333,1600MHZ BANDWIDTH

DDR3 1066MHZ
DDR3 clock=533MHZ
DDR3 single channel bandwidth=533x2x8Byte=8.5GB/s
DDR3 dual channel bandwidth=533x2x2x8Byte=17GB/s

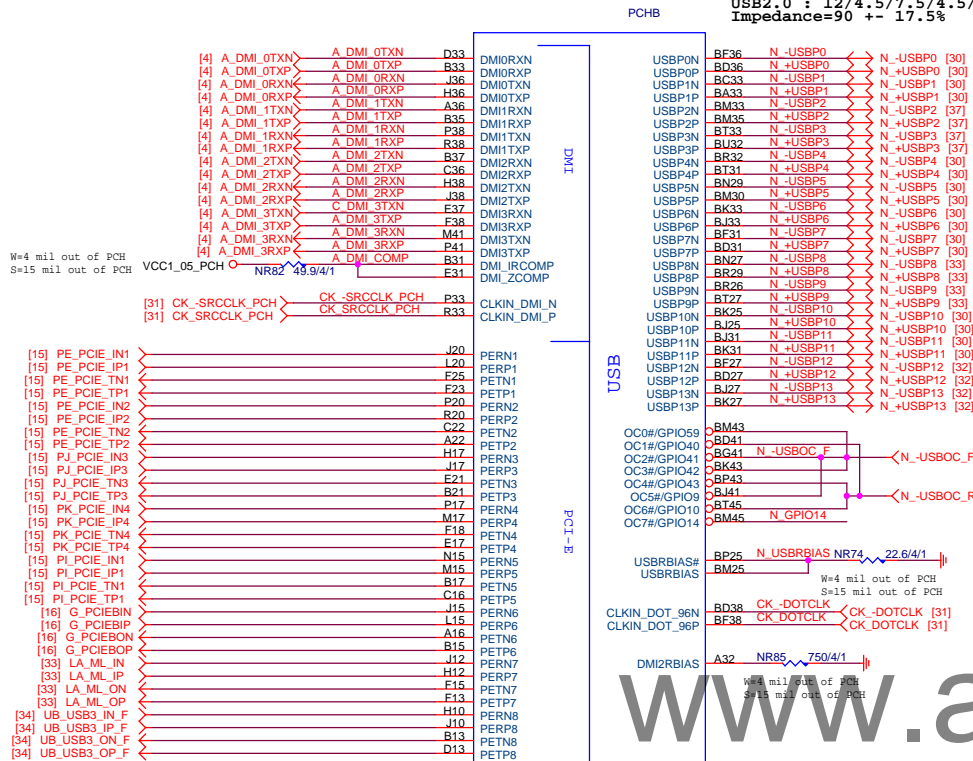
DDR3 1333MHZ
DDR3 clock=667MHZ
DDR3 single channel bandwidth=10.6GB/s
DDR3 dual channel bandwidth=21GB/s

DDR3 1600MHZ
DDR3 clock=800MHZ
DDR3 single channel bandwidth=12.8GB/s
DDR3 dual channel bandwidth=25.6GB/s



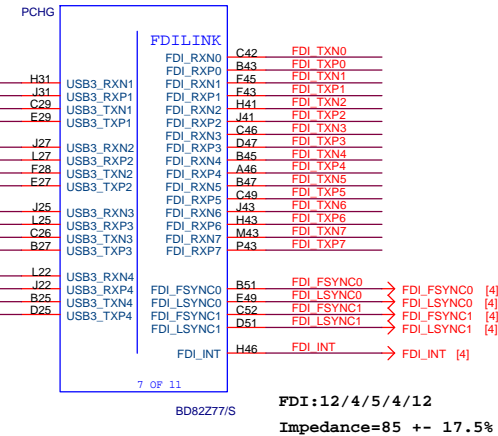
USB3.0:20/5/7/5/20 (breakout min 8/4/4/4/8) ; ONLY 3 VIAS
Impedance=85 +- 17.5%
Back Panel < 10000 MILS
Front Panel < 6000 MILS

USB2.0 : 12/4.5/7/5/4.5/12 (breakout min 8/4/4/4/8)
Impedance=90 +- 17.5%



OC[3:0]# for Device 29 (ports 0-7)
OC[7:4]# for Device 26 (ports 8-13)

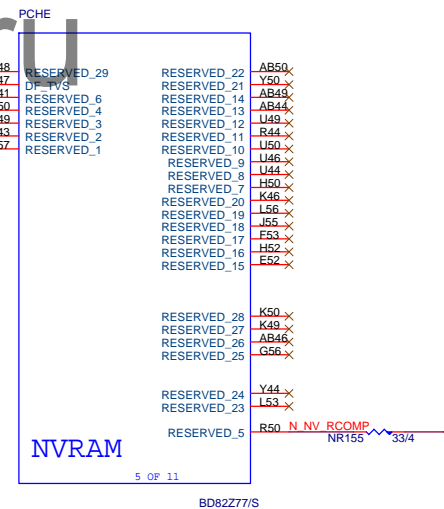
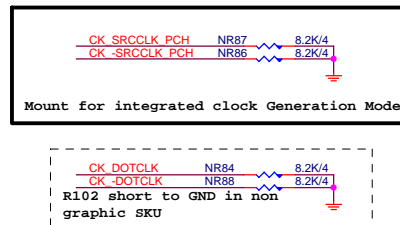
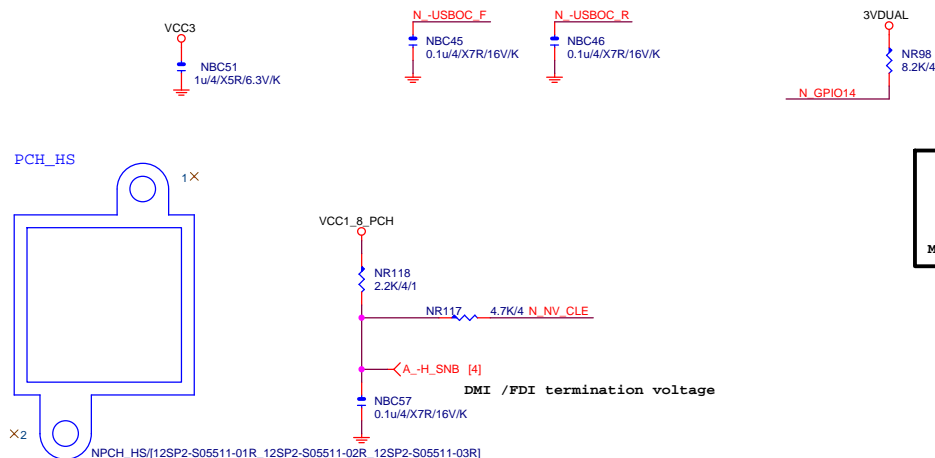
USB OC#	Configure
OC0#	USB0,1
OC1#	USB2,3
OC2#	USB4,5
OC3#	USB6,7
OC4#	USB8,9
OC5#	USB10,11
OC6#	USB12,13
OC7#	Not Use

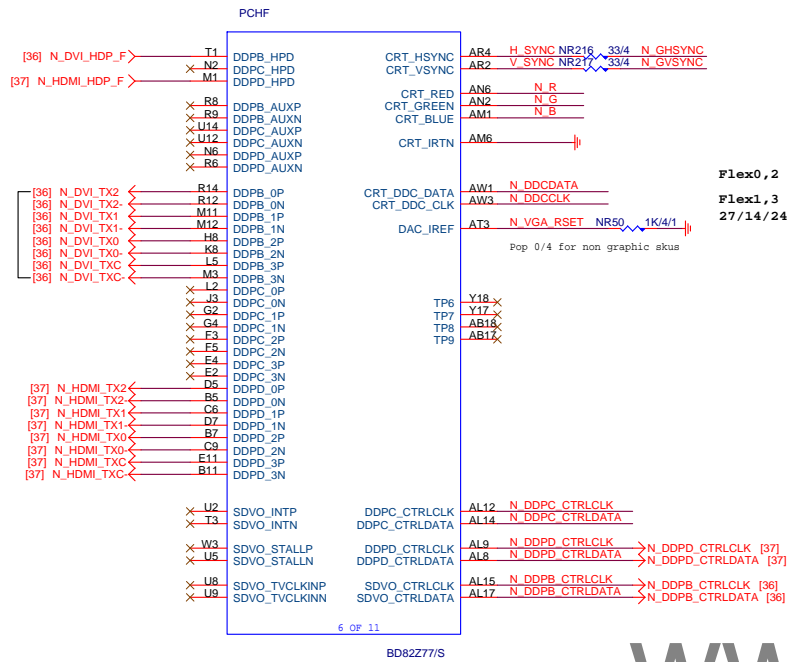


FDI_TXP0_7I >>> FDI_TXP0[0..7] [4]
FDI_TXN0_7I >>> FDI_TXN0[0..7] [4]

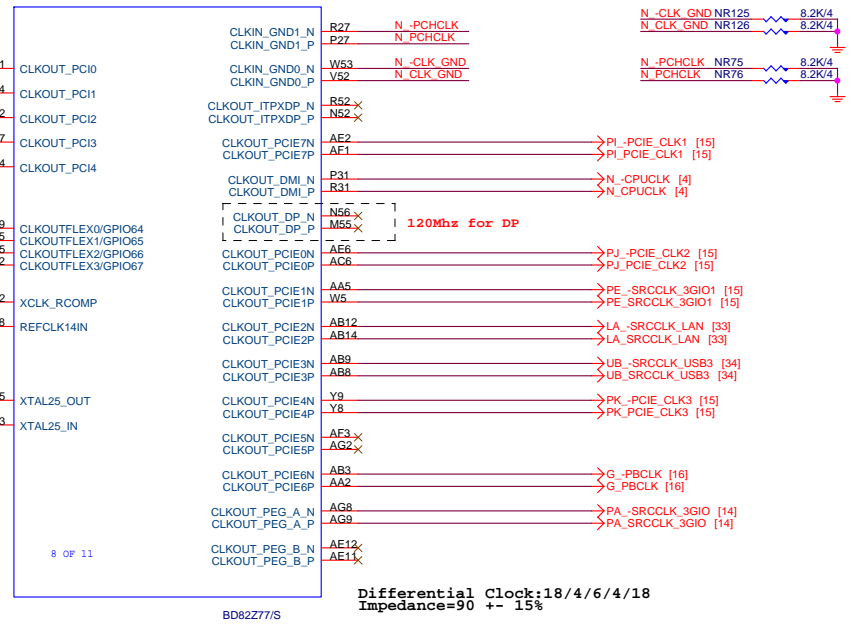
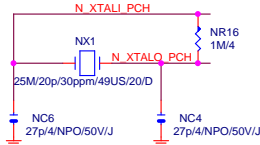
FDI:12/4/5/4/12
Impedance=85 +- 17.5%

PCIEX1:16/5/5/5/16 (breakout min 8/4/4/4/8)
Impedance=80 +- 17.5%





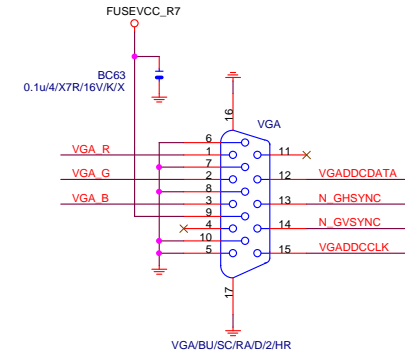
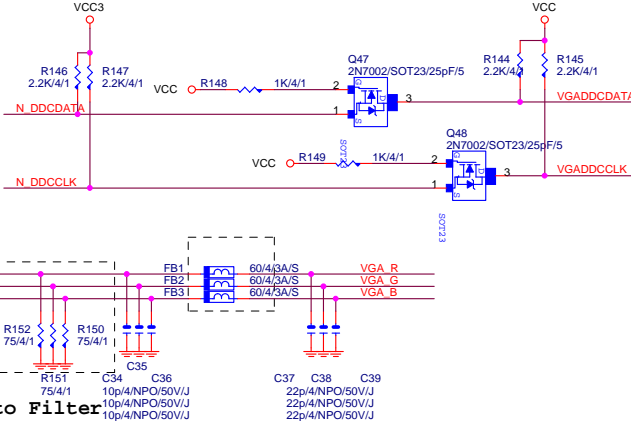
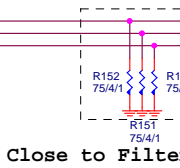
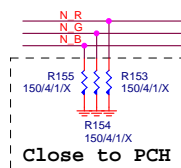
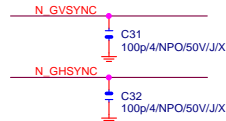
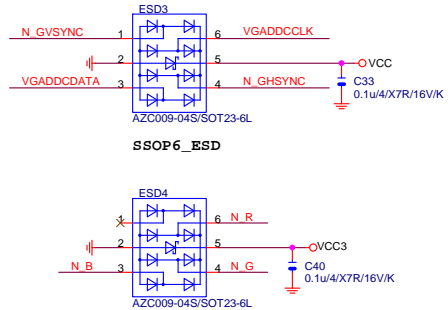
Flex0,2 : 33MHZ
Flex1,3 :
27/14/24/48/25MHZ



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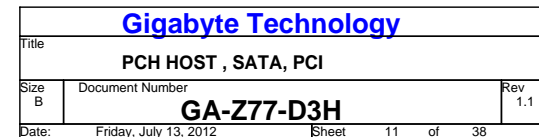


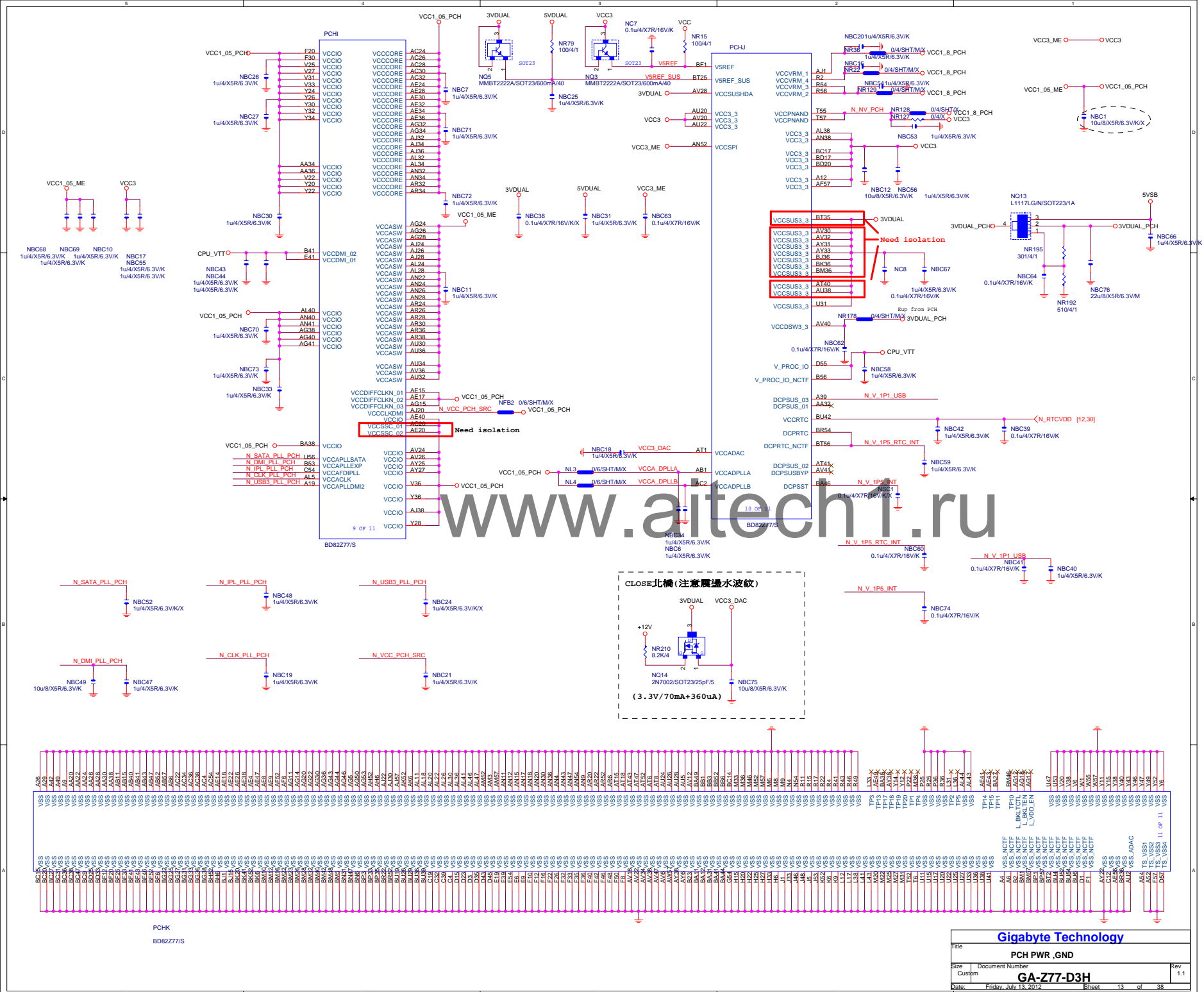
Check if NC for P67 non graphic chip

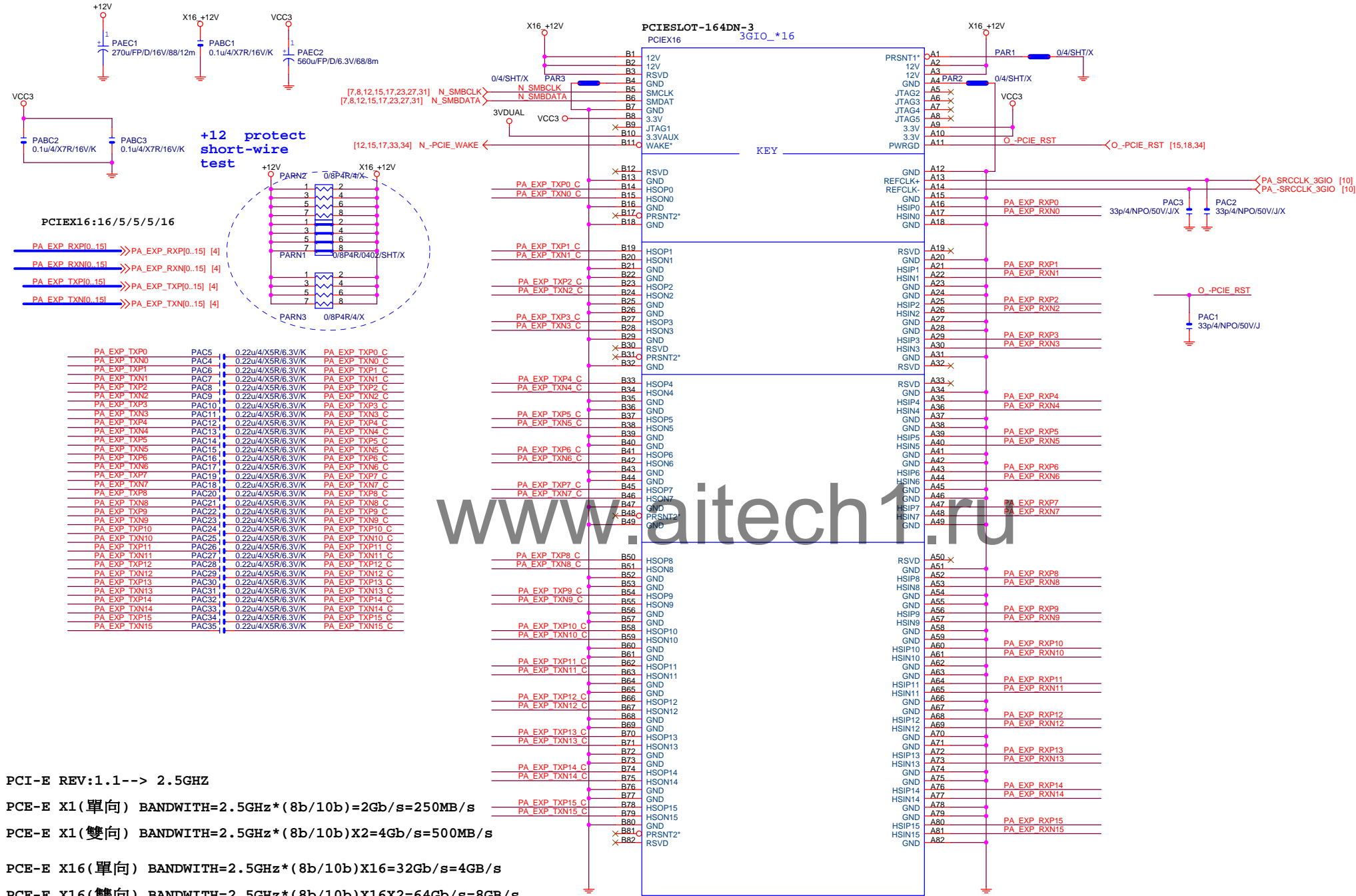


Gigabyte Technology			
Title			
PCH DISPLAY ,CLK BUFFER			
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SATA2 : 15/7.5/4.5/7.5/15 (breakout min 8/4/4/4/8)
Impedance=90 +- 17.5%







PCI-E REV:1.1--> 2.5GHZ

PCE-E X1(單向) BANDWITH=2.5GHz*(8b/10b)=2Gb/s=250MB/s

PCE-E X1(雙向) BANDWITH=2.5GHz*(8b/10b)X2=4Gb/s=500MB/s

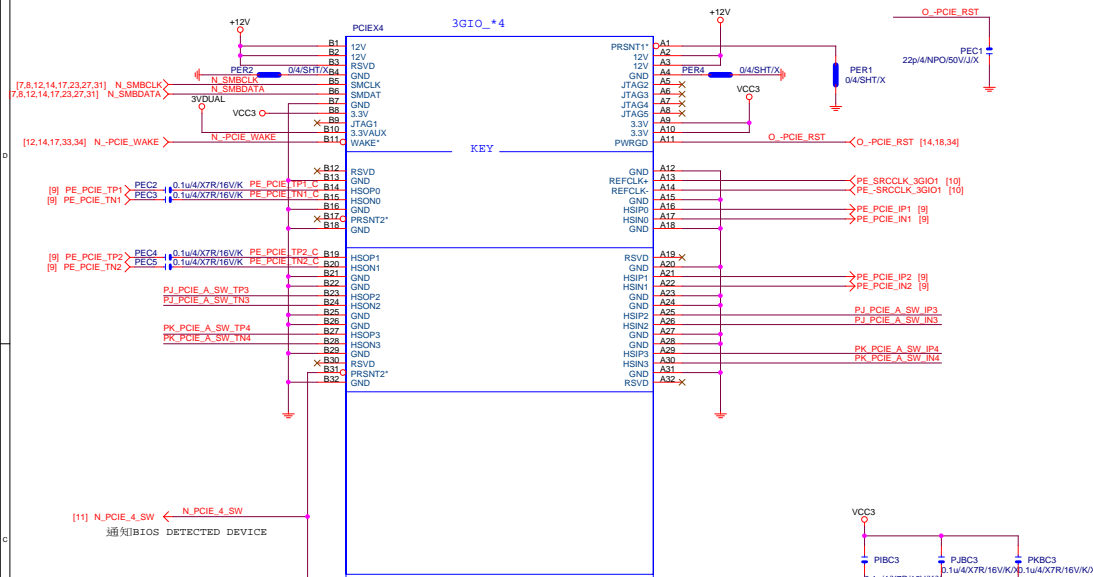
PCE-E X16(單向) BANDWITH=2.5GHz*(8b/10b)X16=32Gb/s=4GB/s

PCE-E X16(雙向) BANDWITH=2.5GHz*(8b/10b)X16X2=64Gb/s=8GB/s

PCI-E REV:2.0--> 5GHZ

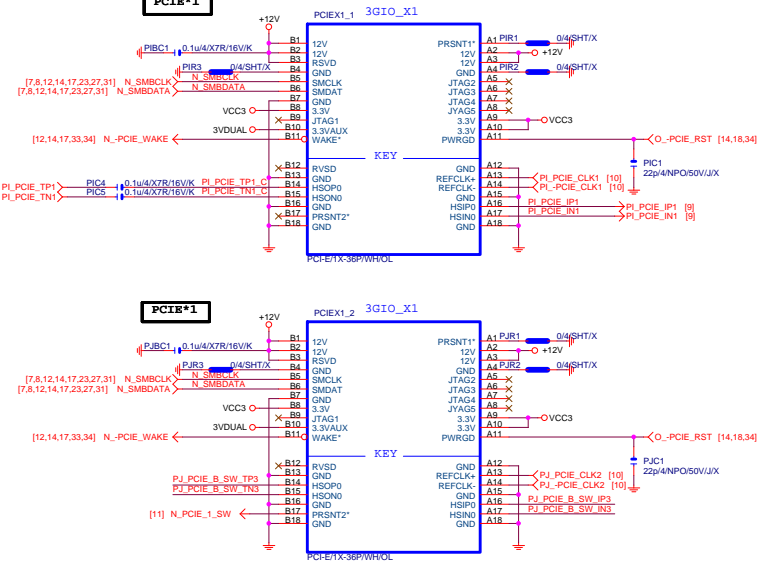
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Title			
PCI EXPRESS * 16			
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PCIE*4

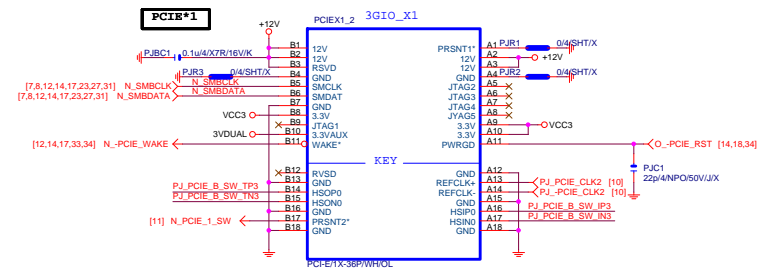


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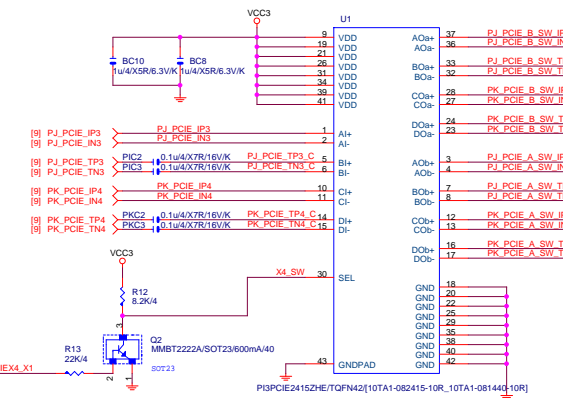
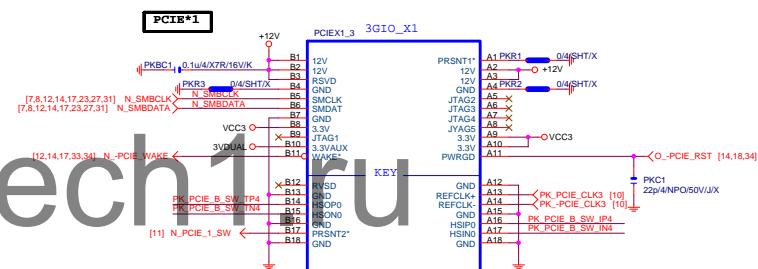
PCIE*1



PCIE*1



PCIE*1



	N_PCIE_4_SW (PCH GPIO38)	PCIEX4_X1 (SIO GPIO26)
PCIEX1,PCIEX4 --> X1 (Default)	H	H
PCIEX4 No devices PCIEX4 --> X1	H	H
PCIEX4 Have devices PCIEX4 --> X4	L	L
PCIEX1_2/PCIEX1_3 --> N/A		

Function

SEL

x1--> x0a

L₁PCIEX4 SLOT-->X1

x1--> x0b

H₁PCIEX4 SLOT-->X4

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PCIE X1 1.2

GA-Z77-D3H

Title

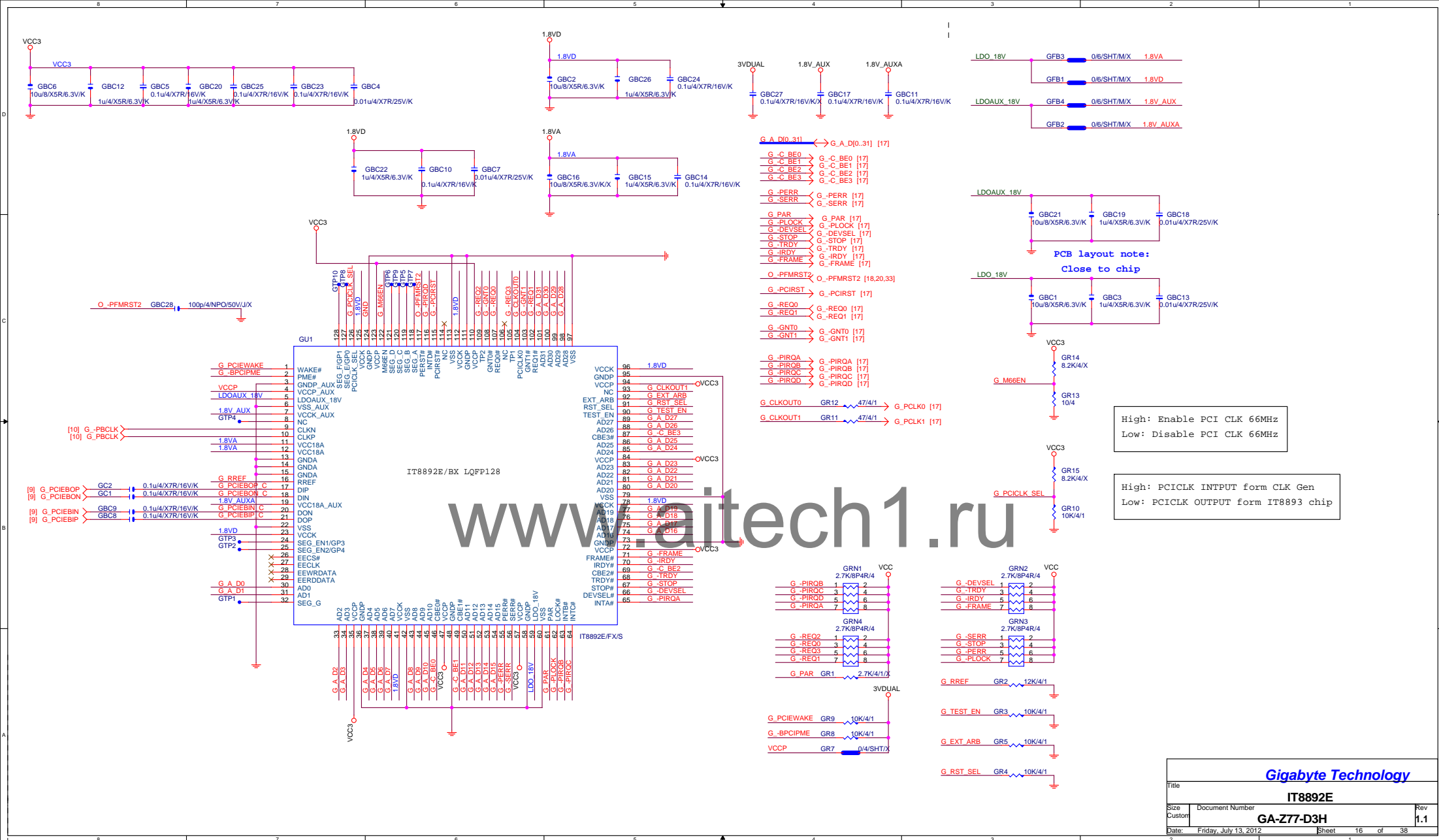
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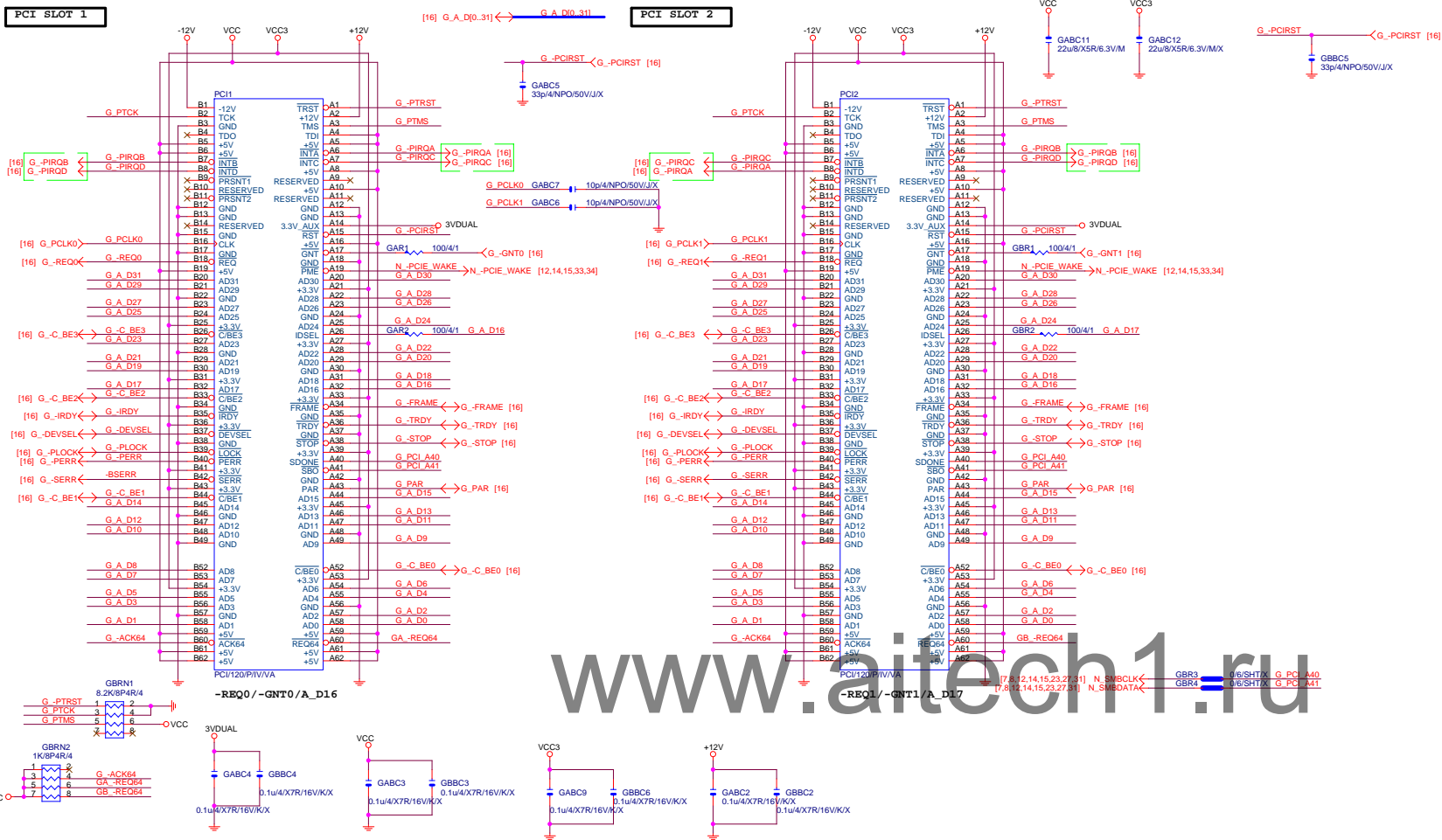
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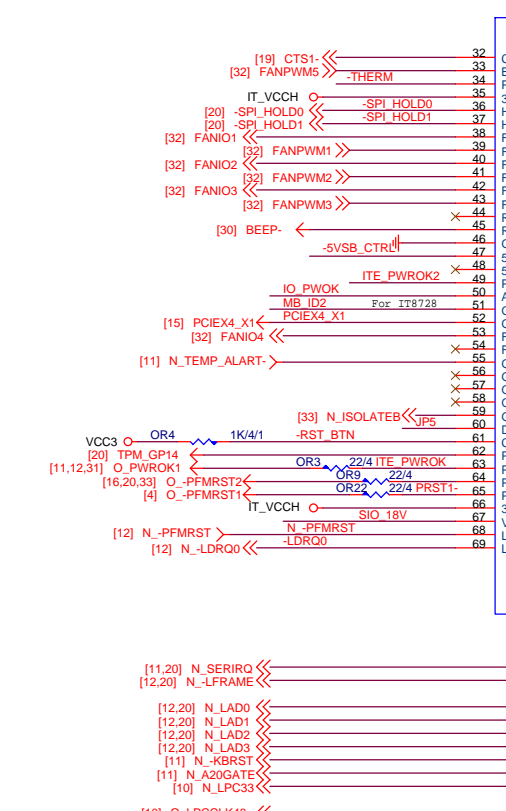
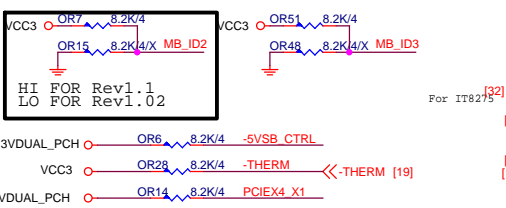
PCI SLOT 1

PCI SLOT 2

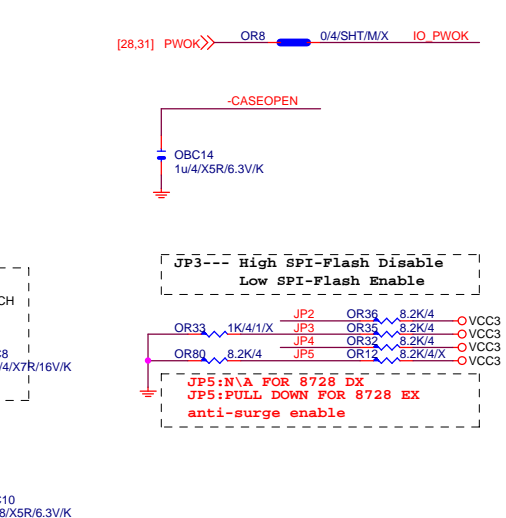
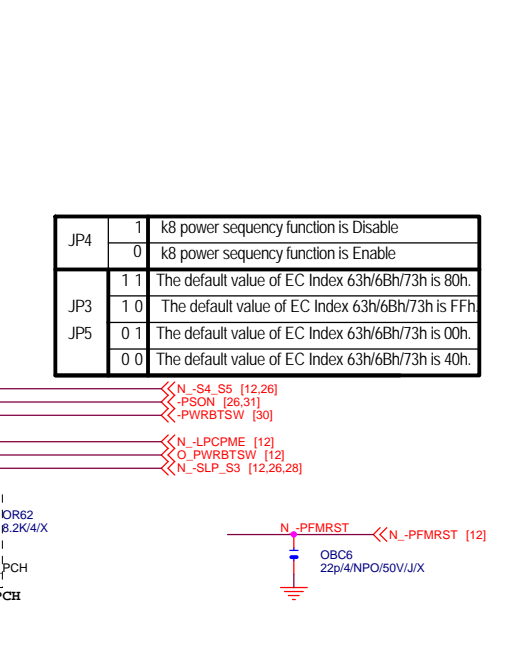
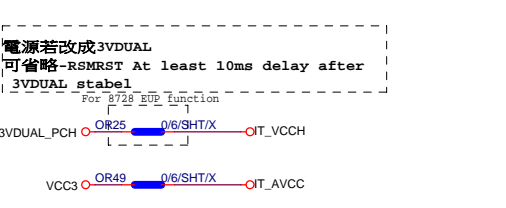
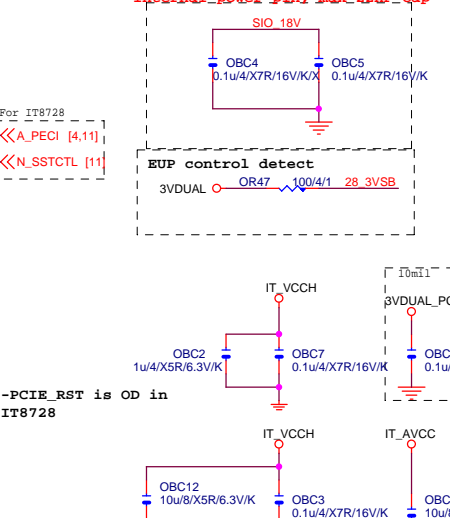
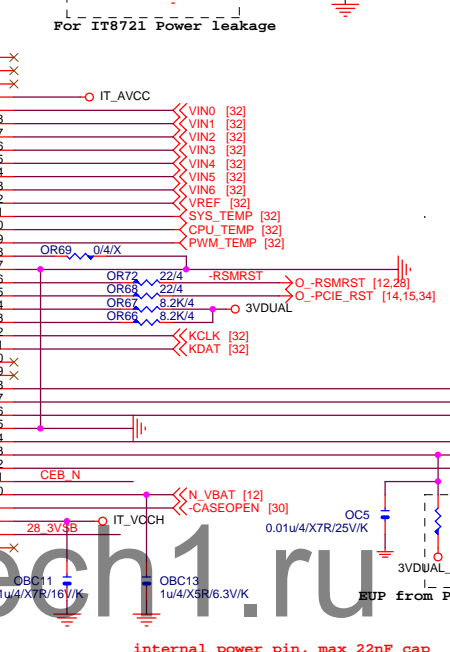
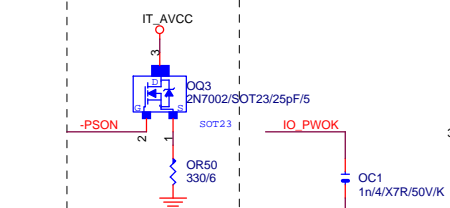
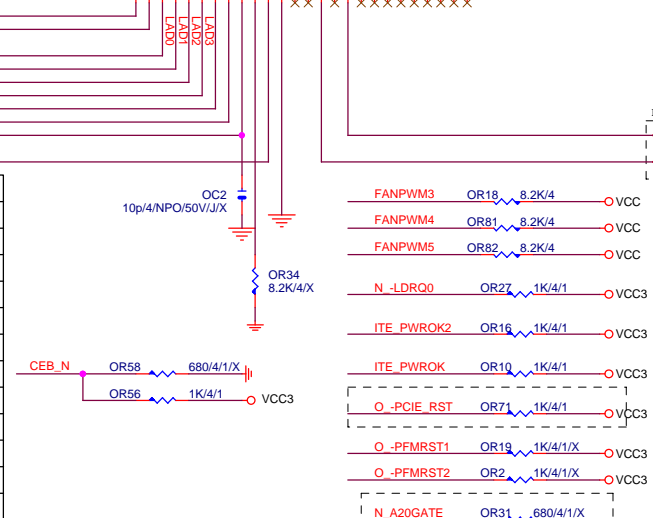
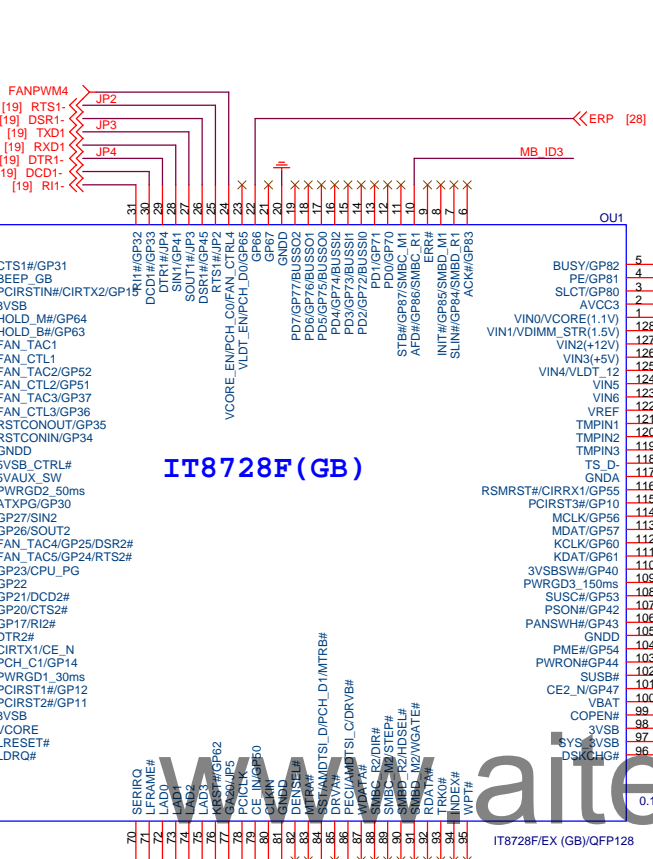


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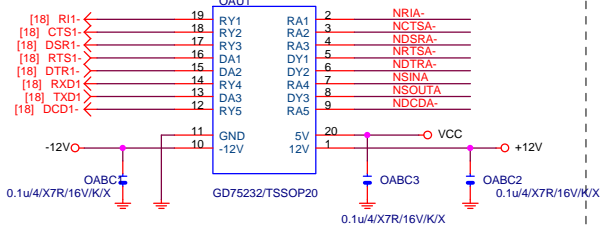


	IT8721	IT8728
PIN121	FAN_CTL4/VID_TURBO	VCORE_EN/PCH_C0
PIN120	VDDA_EN	VLDT_EN/PCH_D0
PIN19	GP30	ATXPG
PIN31	GP14	PCH_C1
PIN53	SST/AMDTSI_D/PECI_AVA/MTRB#/PCH_D	SST/AMDTSI_D/MTRB#/PCH_D1
PIN55	PECI/AMDTSI_C/DRVB#/PCH_C	PECI/AMDTSI_C/DRVB#
PIN66	GP47	SYS_3VSB
PIN70	SYS_3VSB	GP47
PIN95	VIN3/ATXPG	VIN2(VCC5)
PIN96	VIN2	VIN1(VCC12)
PIN97	VIN1(VCC5)	VIN1/VDIMM_STR(1.5V)
PIN98	VIN0(VCC12)	VIN0/VCORE(1.1V)

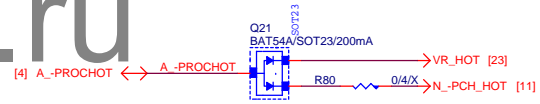
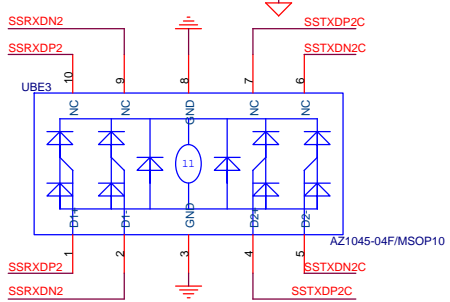
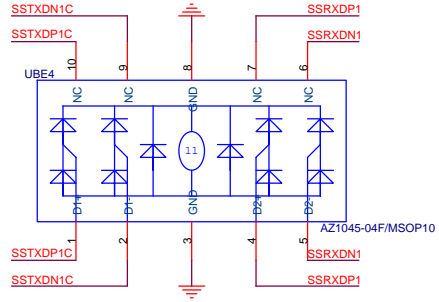
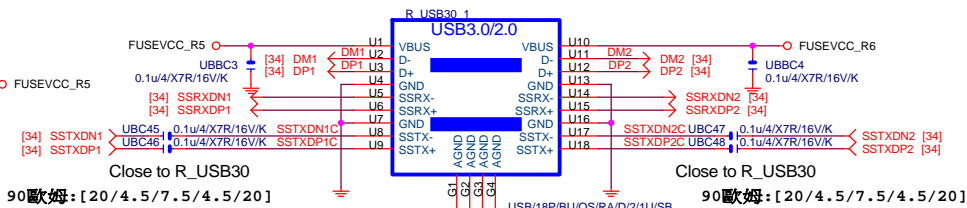
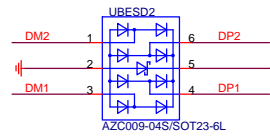
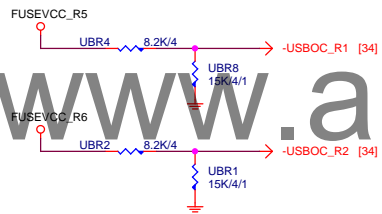
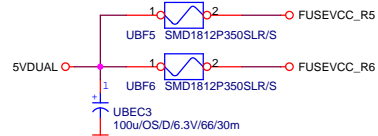
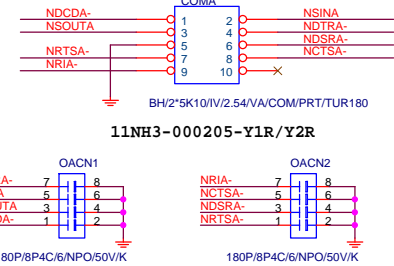
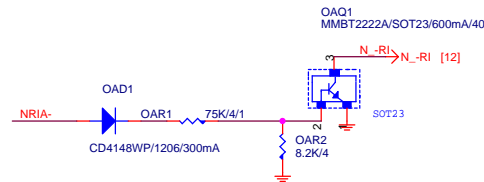


Gigabyte Technology			
Title			
ITE 8728 LPC IO			
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B		1.1	
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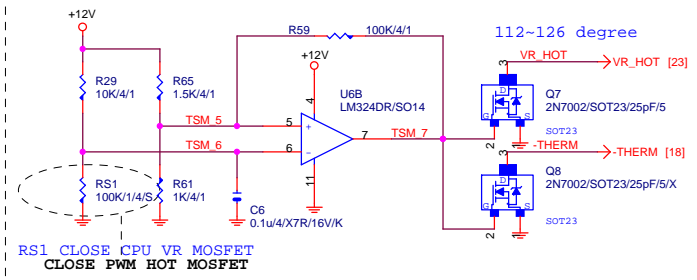
COMA



COM RI



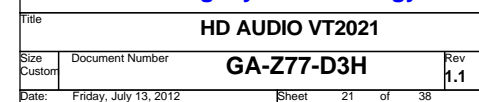
-PROHOT



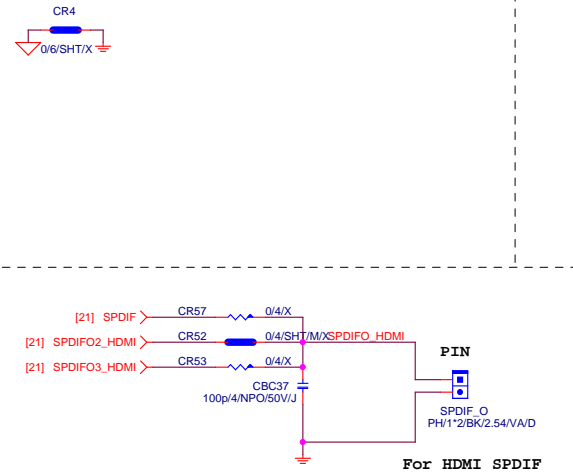
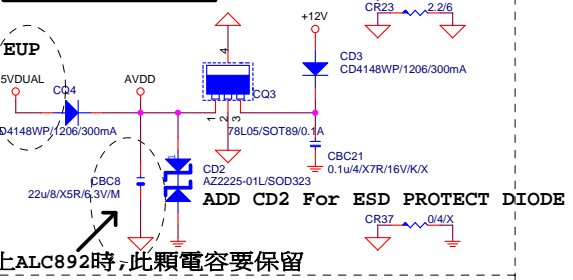
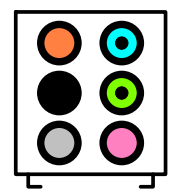
Gigabyte Technology

Title			
COM & PROHOT/Dynamic O.C.			
Size	Document Number	Rev	
Custom		1.1	
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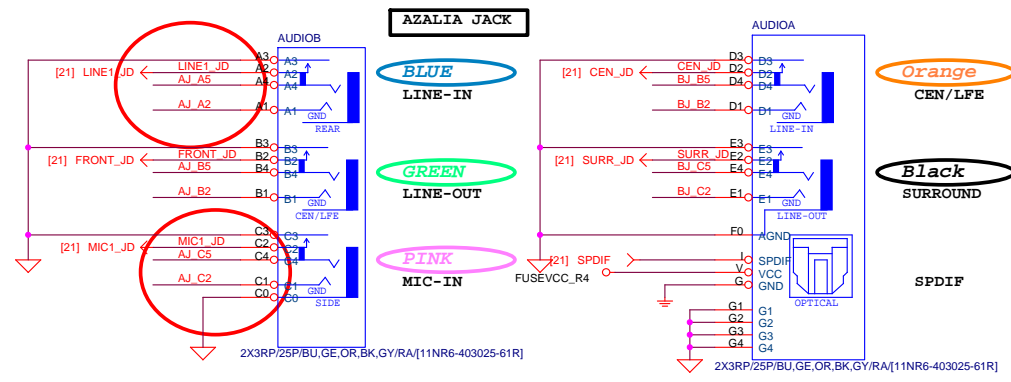
CR7/CR9/CR5/CR13/
CR29/CR32/CR46/CR19/
CR50/CR41/CR2/CR11/
CR14/CR24



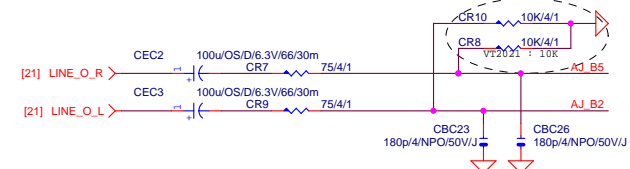
CODEC POWER/EMI PAD

AZALIA JACK
BTX AZALIA CONNECTOR

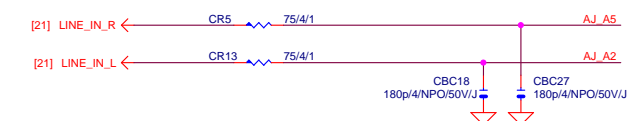
11NR6-403007-21R



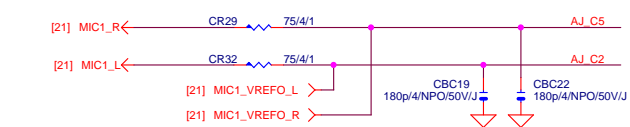
LINE-OUT



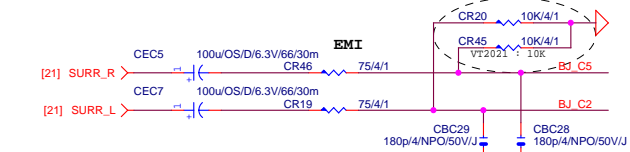
LINE-IN



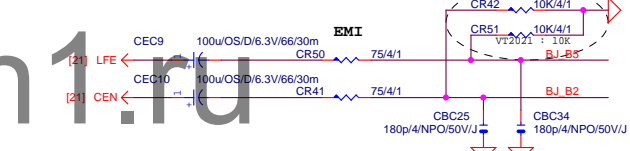
MIC-IN



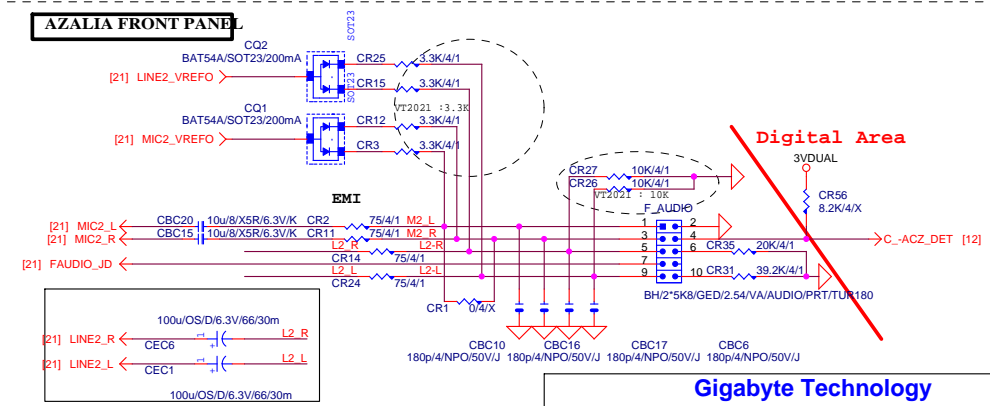
SURROUND



CEN/LFE



AZALIA FRONT PANEL



Digital Area

Gigabyte Technology

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VCORE Phase 1,3

DA_DR2 14k1
VCC
VCC
VCC
DA_DR3 8.2k4
DA_DC3 0.1uF/6X7R25V/K
DA_DR5 15k
DA_DC1 1uF/6X7R16V/K
DA_DR6 2.2k
DA_DR7 0.45H/7X
DA_DR8 0.45H/7X
DA_DR9 15k
DA_DC2 1uF/6X7R16V/K
DA_DR10 0.1uF/6X7R25V/K
DA_DR11 0.45H/7X
DA_DC4 1uF/4X7R50V/K
DA_DC5 1uF/4X7R16V/K
DA_DR12 15k
DA_DC6 1uF/6X7R16V/K
DA_DR13 15k
DA_DC7 0.1uF/6X7R25V/K
DA_DR14 15k
DA_DC8 1uF/6X7R16V/K
DA_DR15 15k
DA_DC9 1uF/6X7R16V/K
DA_DR16 15k
DA_DC10 1uF/6X7R16V/K
DA_DR17 15k
DA_DC11 1uF/6X7R16V/K
DA_DR18 15k
DA_DC12 1uF/6X7R16V/K
DA_DR19 15k
DA_DC13 1uF/6X7R16V/K
DA_DR20 15k
DA_DC14 1uF/6X7R16V/K
DA_DR21 15k
DA_DC15 1uF/6X7R16V/K
DA_DR22 15k
DA_DC16 1uF/6X7R16V/K
DA_DR23 15k
DA_DC17 1uF/6X7R16V/K
DA_DR24 15k
DA_DC18 1uF/6X7R16V/K
DA_DR25 15k
DA_DC19 1uF/6X7R16V/K
DA_DR26 15k
DA_DC20 1uF/6X7R16V/K
DA_DR27 15k
DA_DC21 1uF/6X7R16V/K
DA_DR28 15k
DA_DC22 1uF/6X7R16V/K
DA_DR29 15k
DA_DC23 1uF/6X7R16V/K
DA_DR30 15k
DA_DC24 1uF/6X7R16V/K
DA_DR31 15k
DA_DC25 1uF/6X7R16V/K
DA_DR32 15k
DA_DC26 1uF/6X7R16V/K
DA_DR33 15k
DA_DC27 1uF/6X7R16V/K
DA_DR34 15k
DA_DC28 1uF/6X7R16V/K
DA_DR35 15k
DA_DC29 1uF/6X7R16V/K
DA_DR36 15k
DA_DC30 1uF/6X7R16V/K
DA_DR37 15k
DA_DC31 1uF/6X7R16V/K
DA_DR38 15k
DA_DC32 1uF/6X7R16V/K
DA_DR39 15k
DA_DC33 1uF/6X7R16V/K
DA_DR40 15k
DA_DC34 1uF/6X7R16V/K
DA_DR41 15k
DA_DC35 1uF/6X7R16V/K
DA_DR42 15k
DA_DC36 1uF/6X7R16V/K
DA_DR43 15k
DA_DC37 1uF/6X7R16V/K
DA_DR44 15k
DA_DC38 1uF/6X7R16V/K
DA_DR45 15k
DA_DC39 1uF/6X7R16V/K
DA_DR46 15k
DA_DC40 1uF/6X7R16V/K
DA_DR47 15k
DA_DC41 1uF/6X7R16V/K
DA_DR48 15k
DA_DC42 1uF/6X7R16V/K
DA_DR49 15k
DA_DC43 1uF/6X7R16V/K
DA_DR50 15k
DA_DC44 1uF/6X7R16V/K
DA_DR51 15k
DA_DC45 1uF/6X7R16V/K
DA_DR52 15k
DA_DC46 1uF/6X7R16V/K
DA_DR53 15k
DA_DC47 1uF/6X7R16V/K
DA_DR54 15k
DA_DC48 1uF/6X7R16V/K
DA_DR55 15k
DA_DC49 1uF/6X7R16V/K
DA_DR56 15k
DA_DC50 1uF/6X7R16V/K
DA_DR57 15k
DA_DC51 1uF/6X7R16V/K
DA_DR58 15k
DA_DC52 1uF/6X7R16V/K
DA_DR59 15k
DA_DC53 1uF/6X7R16V/K
DA_DR60 15k
DA_DC54 1uF/6X7R16V/K
DA_DR61 15k
DA_DC55 1uF/6X7R16V/K
DA_DR62 15k
DA_DC56 1uF/6X7R16V/K
DA_DR63 15k
DA_DC57 1uF/6X7R16V/K
DA_DR64 15k
DA_DC58 1uF/6X7R16V/K
DA_DR65 15k
DA_DC59 1uF/6X7R16V/K
DA_DR66 15k
DA_DC60 1uF/6X7R16V/K
DA_DR67 15k
DA_DC61 1uF/6X7R16V/K
DA_DR68 15k
DA_DC62 1uF/6X7R16V/K
DA_DR69 15k
DA_DC63 1uF/6X7R16V/K
DA_DR70 15k
DA_DC64 1uF/6X7R16V/K
DA_DR71 15k
DA_DC65 1uF/6X7R16V/K
DA_DR72 15k
DA_DC66 1uF/6X7R16V/K
DA_DR73 15k
DA_DC67 1uF/6X7R16V/K
DA_DR74 15k
DA_DC68 1uF/6X7R16V/K
DA_DR75 15k
DA_DC69 1uF/6X7R16V/K
DA_DR76 15k
DA_DC70 1uF/6X7R16V/K
DA_DR77 15k
DA_DC71 1uF/6X7R16V/K
DA_DR78 15k
DA_DC72 1uF/6X7R16V/K
DA_DR79 15k
DA_DC73 1uF/6X7R16V/K
DA_DR80 15k
DA_DC74 1uF/6X7R16V/K
DA_DR81 15k
DA_DC75 1uF/6X7R16V/K
DA_DR82 15k
DA_DC76 1uF/6X7R16V/K
DA_DR83 15k
DA_DC77 1uF/6X7R16V/K
DA_DR84 15k
DA_DC78 1uF/6X7R16V/K
DA_DR85 15k
DA_DC79 1uF/6X7R16V/K
DA_DR86 15k
DA_DC80 1uF/6X7R16V/K
DA_DR87 15k
DA_DC81 1uF/6X7R16V/K
DA_DR88 15k
DA_DC82 1uF/6X7R16V/K
DA_DR89 15k
DA_DC83 1uF/6X7R16V/K
DA_DR90 15k
DA_DC84 1uF/6X7R16V/K
DA_DR91 15k
DA_DC85 1uF/6X7R16V/K
DA_DR92 15k
DA_DC86 1uF/6X7R16V/K
DA_DR93 15k
DA_DC87 1uF/6X7R16V/K
DA_DR94 15k
DA_DC88 1uF/6X7R16V/K
DA_DR95 15k
DA_DC89 1uF/6X7R16V/K
DA_DR96 15k
DA_DC90 1uF/6X7R16V/K
DA_DR97 15k
DA_DC91 1uF/6X7R16V/K
DA_DR98 15k
DA_DC92 1uF/6X7R16V/K
DA_DR99 15k
DA_DC93 1uF/6X7R16V/K
DA_DR100 15k
DA_DC94 1uF/6X7R16V/K
DA_DR101 15k
DA_DC95 1uF/6X7R16V/K
DA_DR102 15k
DA_DC96 1uF/6X7R16V/K
DA_DR103 15k
DA_DC97 1uF/6X7R16V/K
DA_DR104 15k
DA_DC98 1uF/6X7R16V/K
DA_DR105 15k
DA_DC99 1uF/6X7R16V/K
DA_DR106 15k
DA_DC100 1uF/6X7R16V/K
DA_DR107 15k
DA_DC101 1uF/6X7R16V/K
DA_DR108 15k
DA_DC102 1uF/6X7R16V/K
DA_DR109 15k
DA_DC103 1uF/6X7R16V/K
DA_DR110 15k
DA_DC104 1uF/6X7R16V/K
DA_DR111 15k
DA_DC105 1uF/6X7R16V/K
DA_DR112 15k
DA_DC106 1uF/6X7R16V/K
DA_DR113 15k
DA_DC107 1uF/6X7R16V/K
DA_DR114 15k
DA_DC108 1uF/6X7R16V/K
DA_DR115 15k
DA_DC109 1uF/6X7R16V/K
DA_DR116 15k
DA_DC110 1uF/6X7R16V/K
DA_DR117 15k
DA_DC111 1uF/6X7R16V/K
DA_DR118 15k
DA_DC112 1uF/6X7R16V/K
DA_DR119 15k
DA_DC113 1uF/6X7R16V/K
DA_DR120 15k
DA_DC114 1uF/6X7R16V/K
DA_DR121 15k
DA_DC115 1uF/6X7R16V/K
DA_DR122 15k
DA_DC116 1uF/6X7R16V/K
DA_DR123 15k
DA_DC117 1uF/6X7R16V/K
DA_DR124 15k
DA_DC118 1uF/6X7R16V/K
DA_DR125 15k
DA_DC119 1uF/6X7R16V/K
DA_DR126 15k
DA_DC120 1uF/6X7R16V/K
DA_DR127 15k
DA_DC121 1uF/6X7R16V/K
DA_DR128 15k
DA_DC122 1uF/6X7R16V/K
DA_DR129 15k
DA_DC123 1uF/6X7R16V/K
DA_DR130 15k
DA_DC124 1uF/6X7R16V/K
DA_DR131 15k
DA_DC125 1uF/6X7R16V/K
DA_DR132 15k
DA_DC126 1uF/6X7R16V/K
DA_DR133 15k
DA_DC127 1uF/6X7R16V/K
DA_DR134 15k
DA_DC128 1uF/6X7R16V/K
DA_DR135 15k
DA_DC129 1uF/6X7R16V/K
DA_DR136 15k
DA_DC130 1uF/6X7R16V/K
DA_DR137 15k
DA_DC131 1uF/6X7R16V/K
DA_DR138 15k
DA_DC132 1uF/6X7R16V/K
DA_DR139 15k
DA_DC13

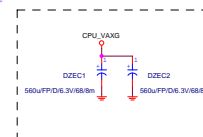
[illegible][illegible]

FUNCTION	MODE	PER MODE	PHASE MODE
0	1	IR ATL	DUAL
1	1	IR ATL	Doubler
0	0	Tri-Sate	DUAL
1	0	Tri-Sate	Doubler
OPEN	0	Tri-Sate	Quad
OPEN	1	IR ATL	Quad

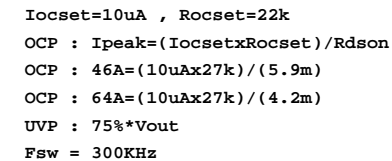
In Quad mode , IC1 pin10 link to IC2 pin10
IC1 pin9 link to IC2 pin9 without PU

FUNCTION	MODE	SWN MODE	PHASE MOD
0	1	IR ATL	DUAL
1	1	IR ATL	Doubles
0	0	Tri-Seate	DUAL
1	0	Tri-Seate	Doubles
OPEN	0	Tri-Seate	Quad
OPEN	1	IR ATL	Quad

In Quad mode , IC1 pin10 link to IC2 pin10
IC1 pin2 link to IC2 pin2 without PU



CPU_VTT

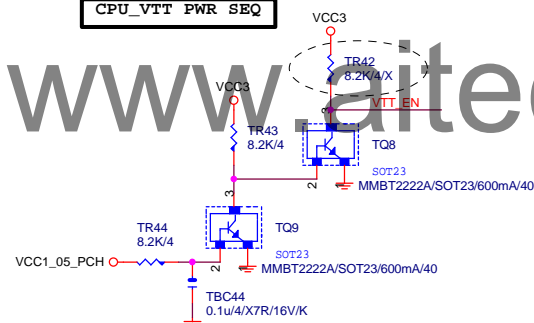


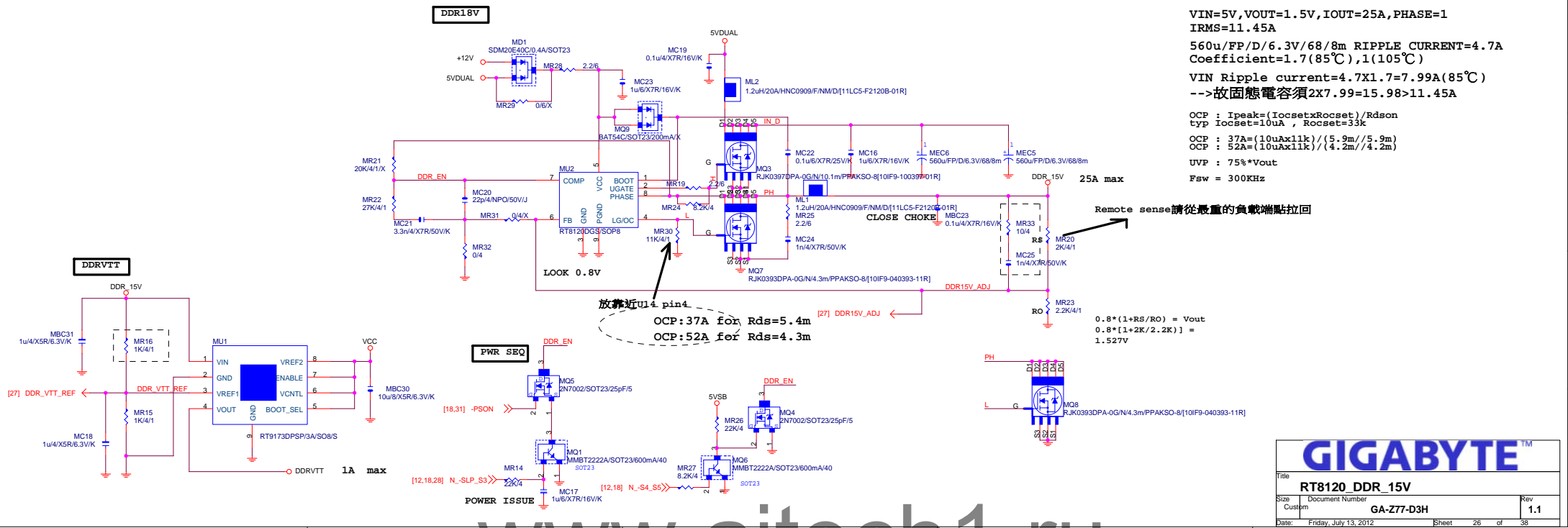
CPU_VTT	PWR	SEQ
---------	-----	-----

	VTT_SEL
HI	1.05V
LO	1.0V

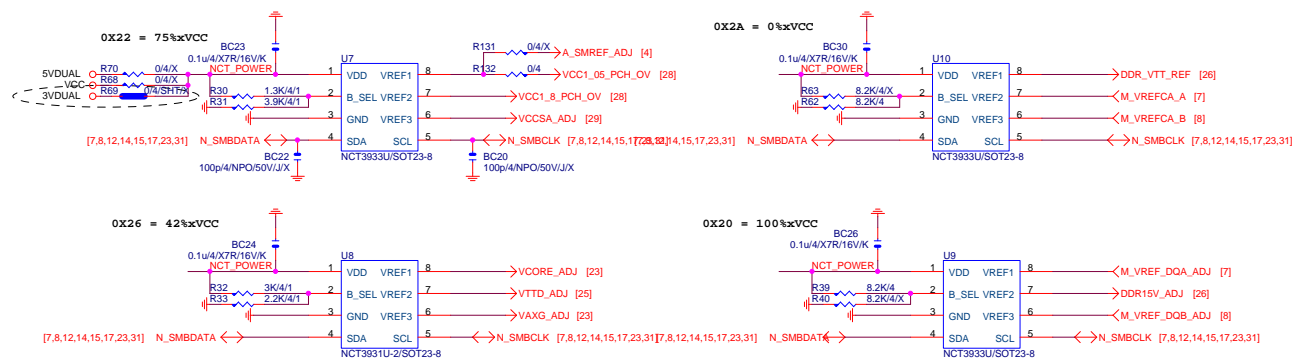
$$0.8 \cdot (1 + R_S/R_O) = V_{out}$$

$$0.8 \cdot [1 + 1.1K/3K] =$$

$$1.09V$$




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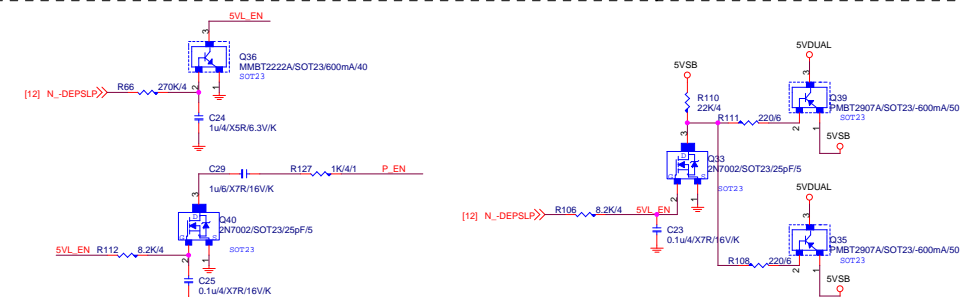
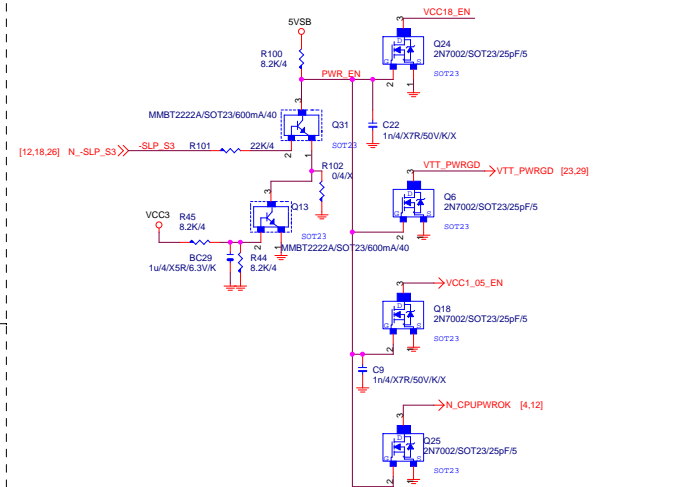
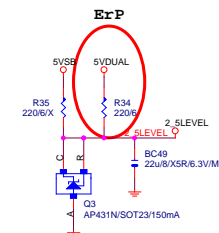
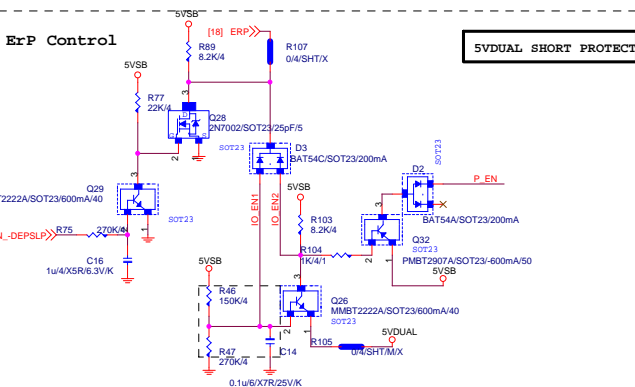
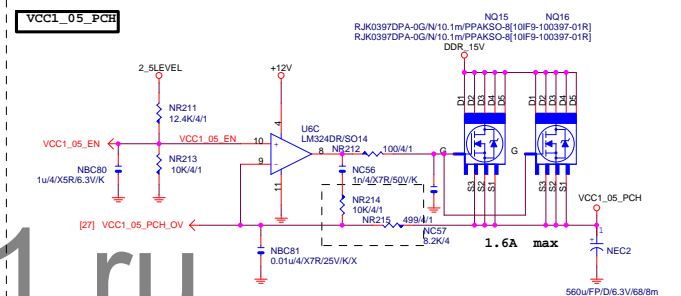
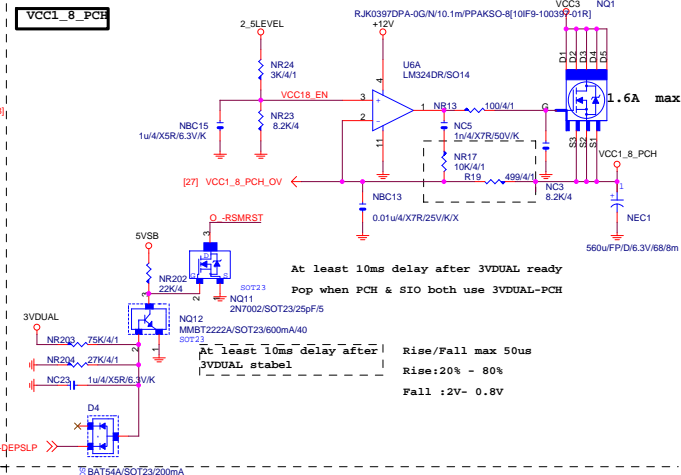
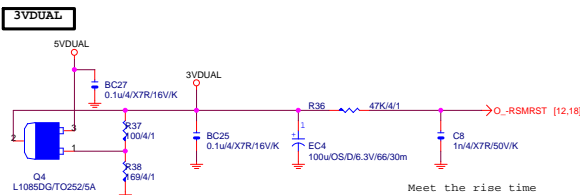
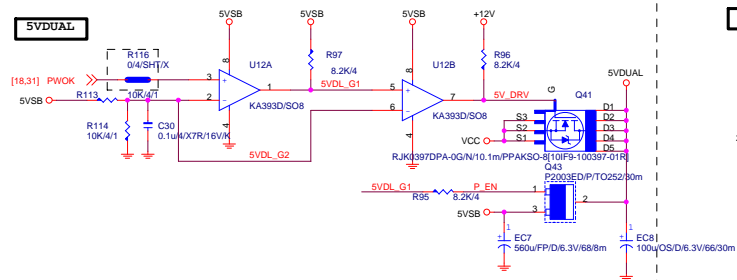


NCT3933	0X2A	0X20	0X22	0X26
VREF1	DDRVTT	VREF_DDRA_DQ	SMREF	VCORE
VREF2	VREF_DDRA_CA	DDR15V	VCC1_8_PCH	CPU_VTT
VREF3	VREF_DDRA_CA	VREF_DDRB_DQ	VCCSA	VAXG

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Title CPU CORE VR-2		
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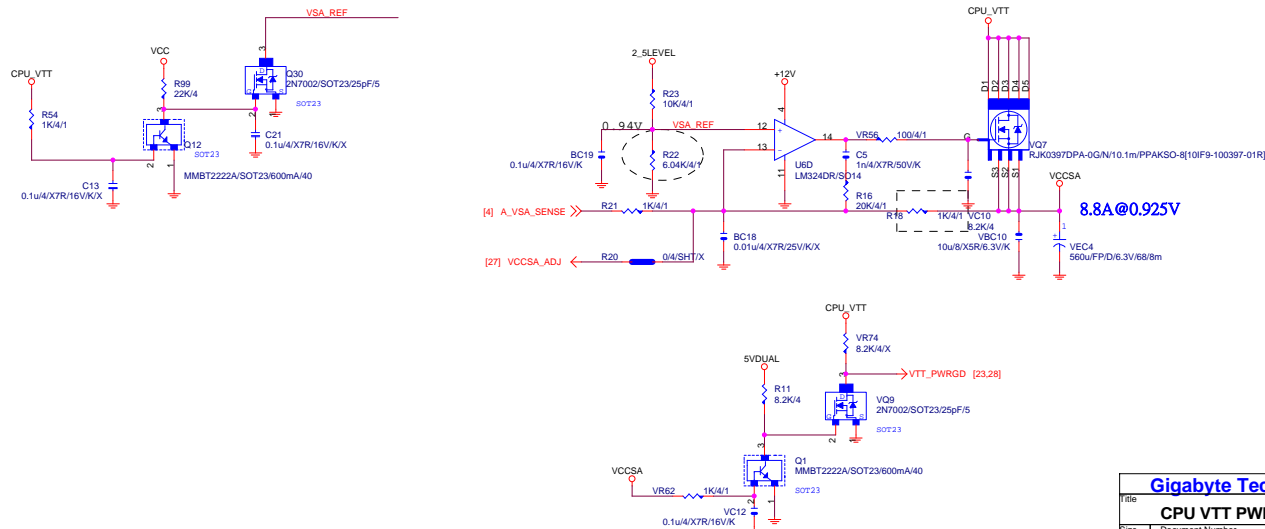
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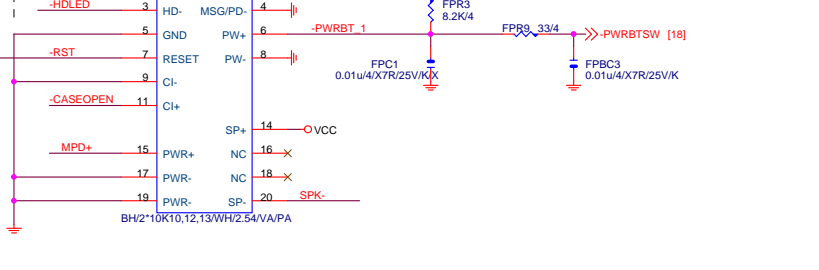
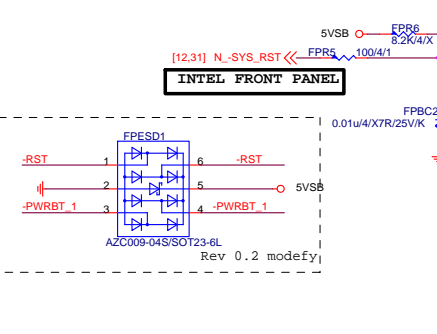
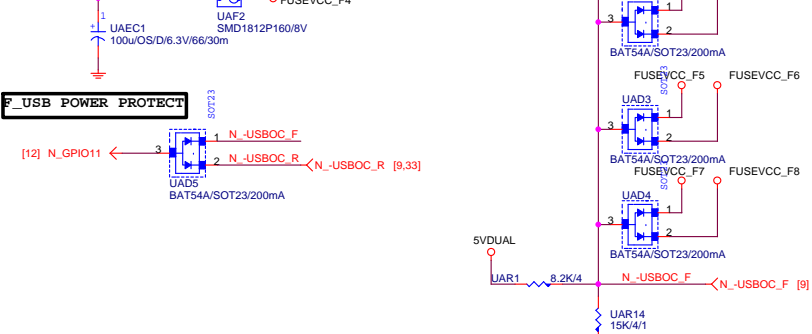
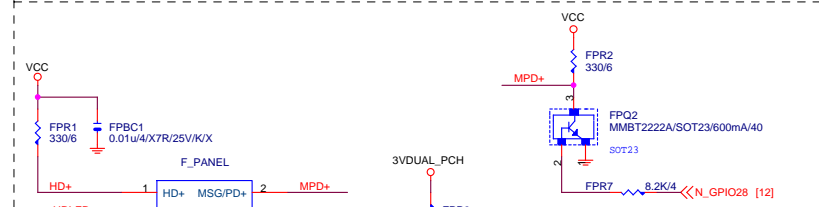
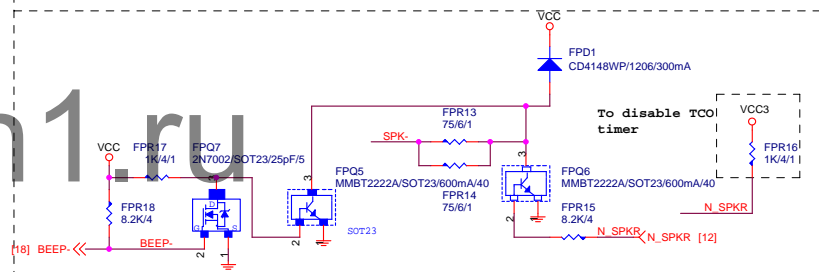
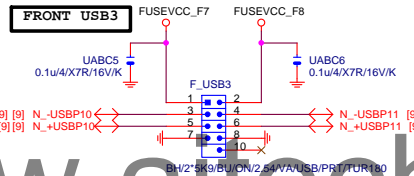
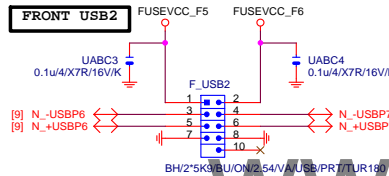
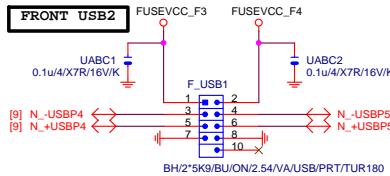
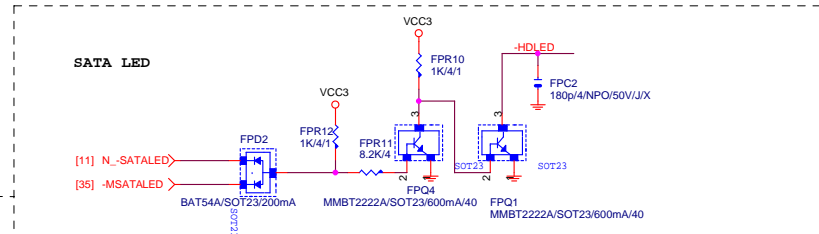
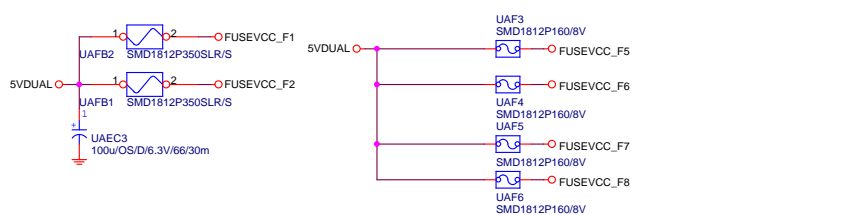
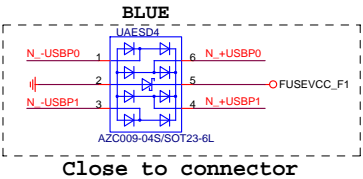
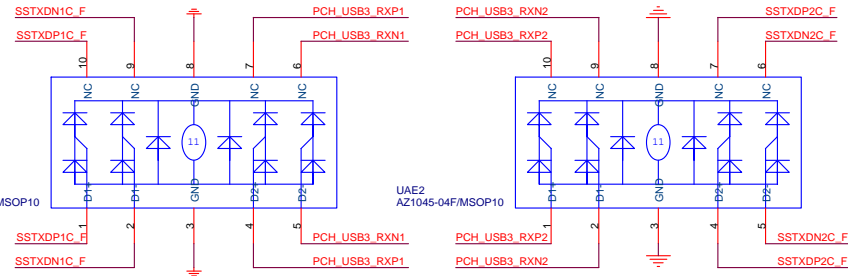
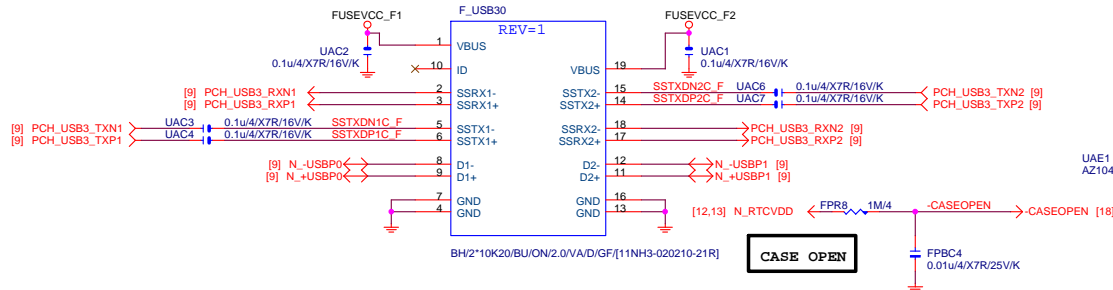
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VCC_SA

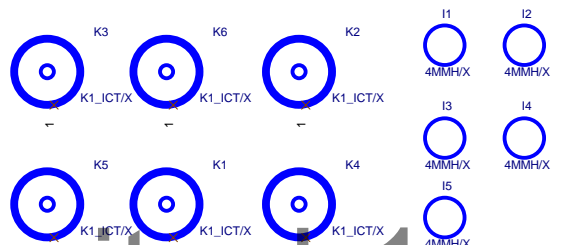
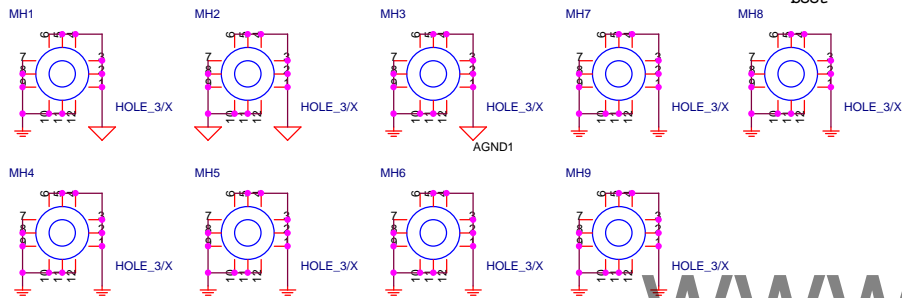
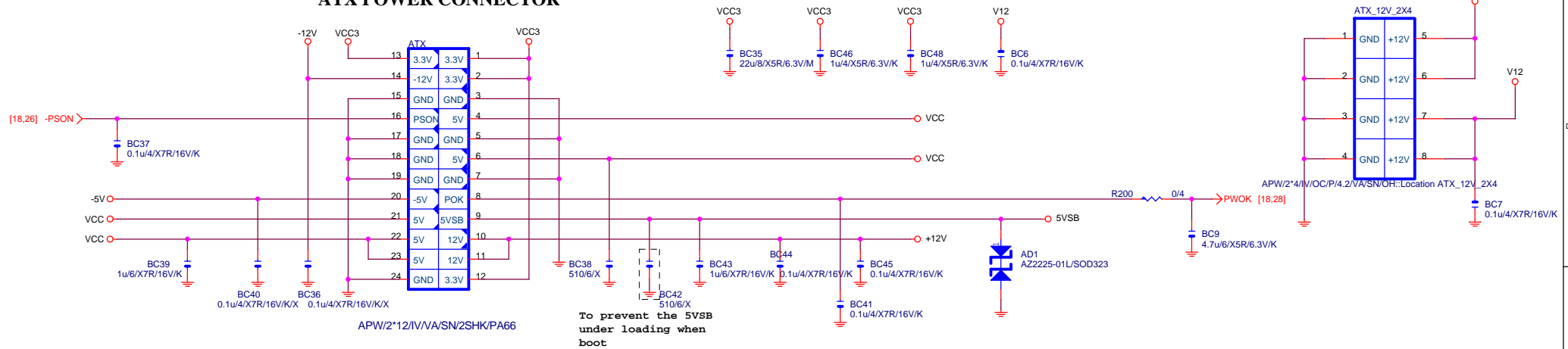


Gigabyte Technology			
CPU VTT PWM_ISL6312			
File	Document Number	Rev	
C	GA-Z77-D3H	1.1	
Date:	Friday, July 13, 2012	Sheet	29 of 38

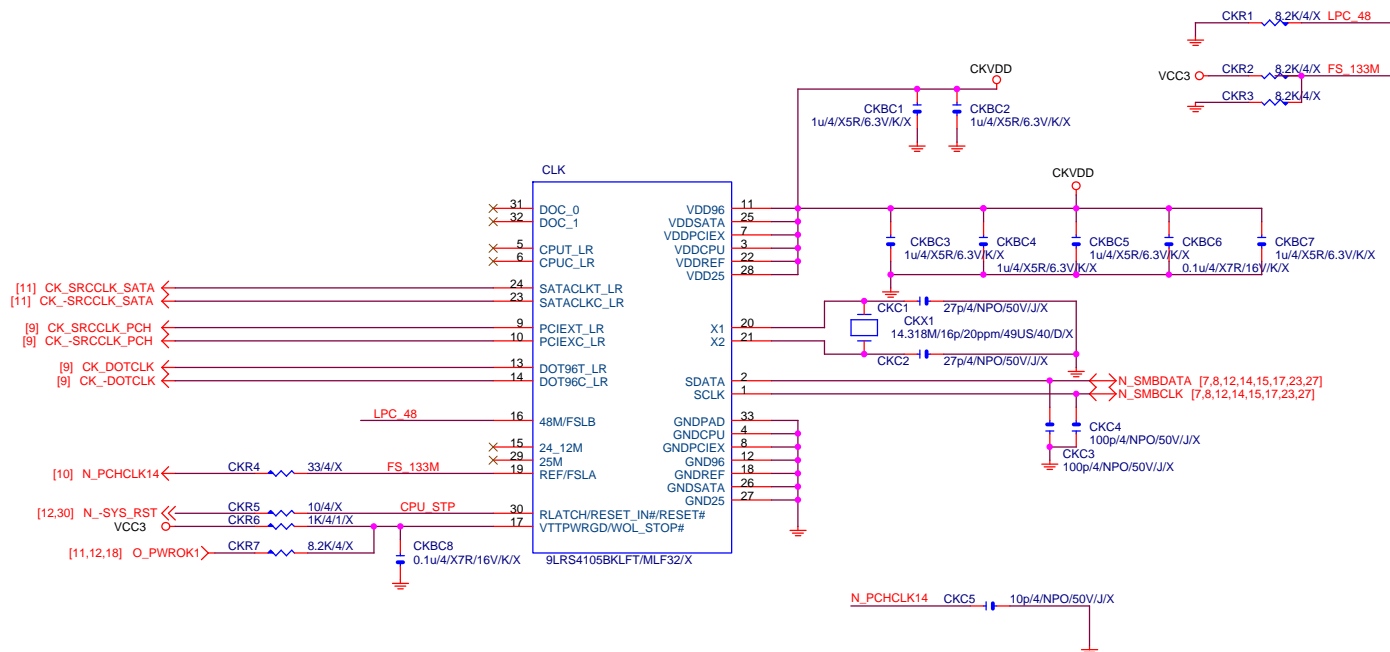
Front USB3.0



ATX POWER CONNECTOR

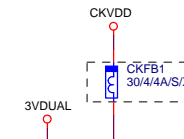


CLK GEN



CPU Frequency Selection

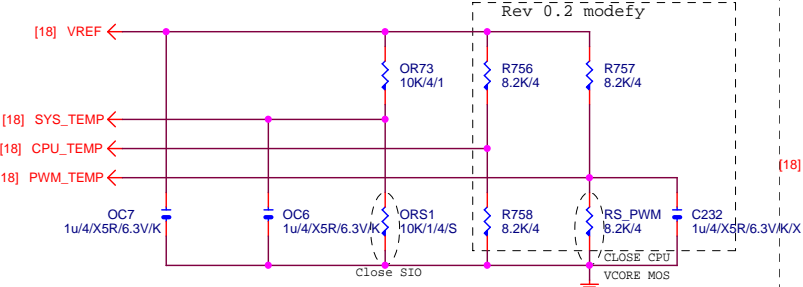
FS	CPU
0	100M <Default>
1	133M



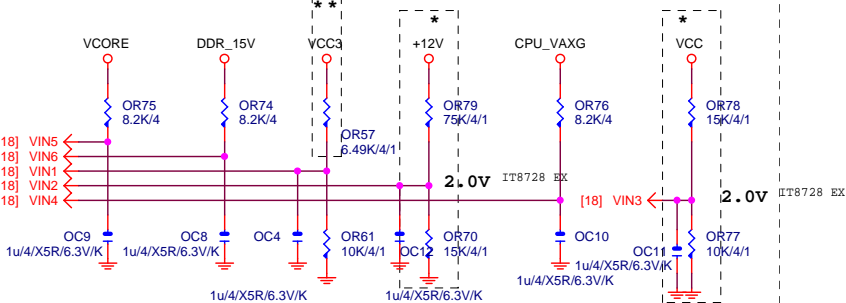
Gigabyte Technology

Title			ATX POWER CONNECTOR
Size	Document Number	GA-Z77-D3H	
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TEMP H/W MONITOR

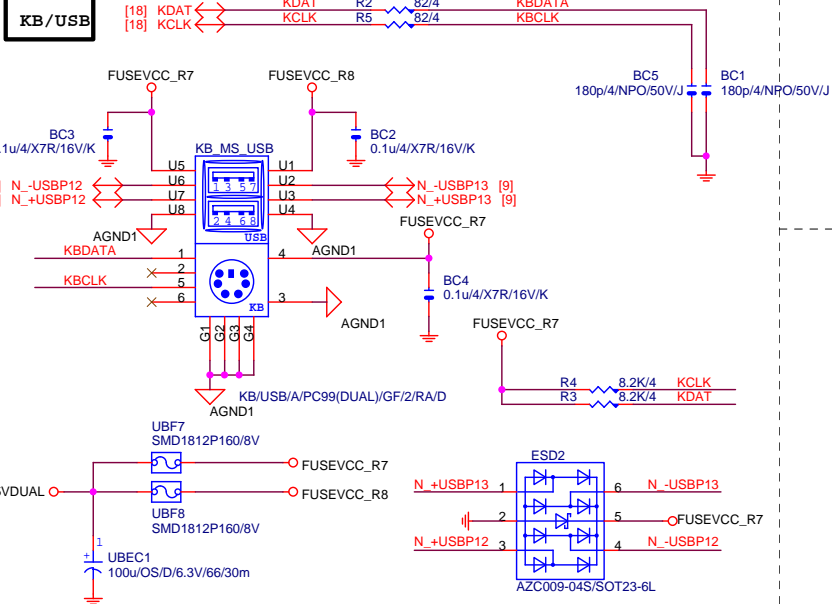


VOLTAGE-- H/W MONITOR

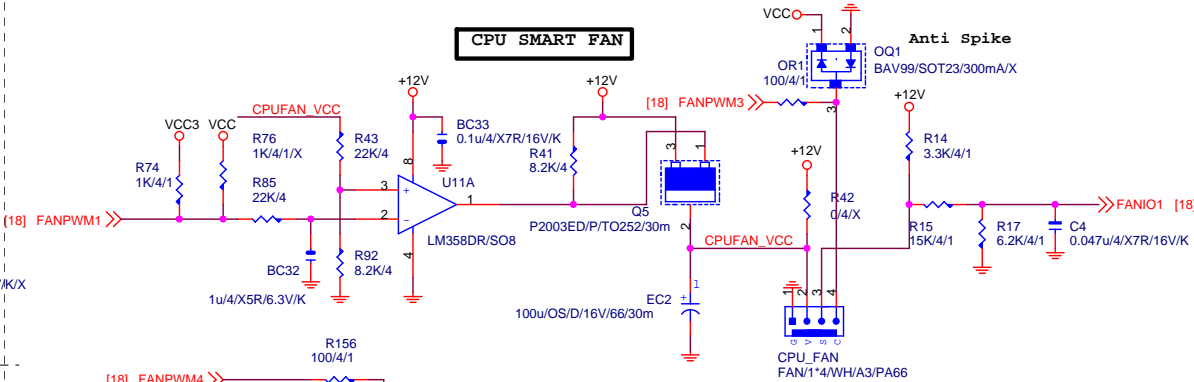


The division voltage of VIN2 & VIN3 must be around 2.9V

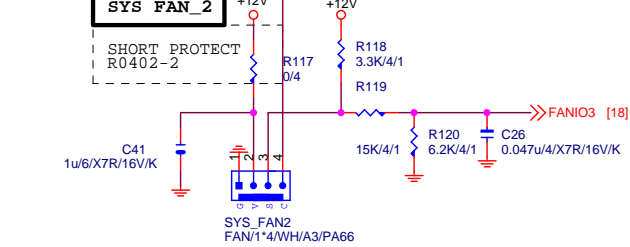
KB/USB



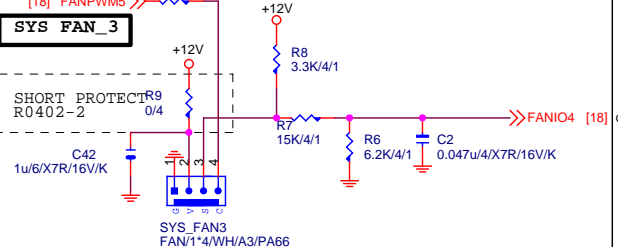
CPU SMART FAN



SYS FAN_2

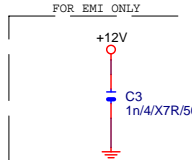
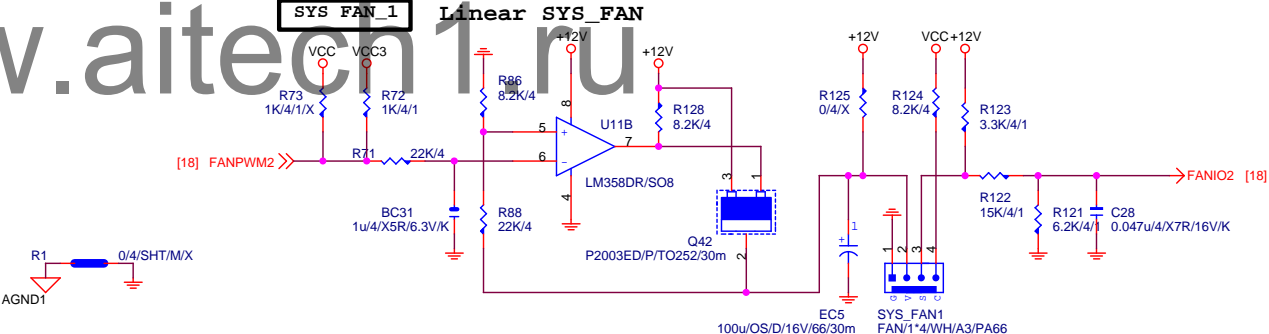


SYS FAN_3



SYS FAN_1

Linear SYS_FAN



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HWM,KB/MS, FAN CTRL

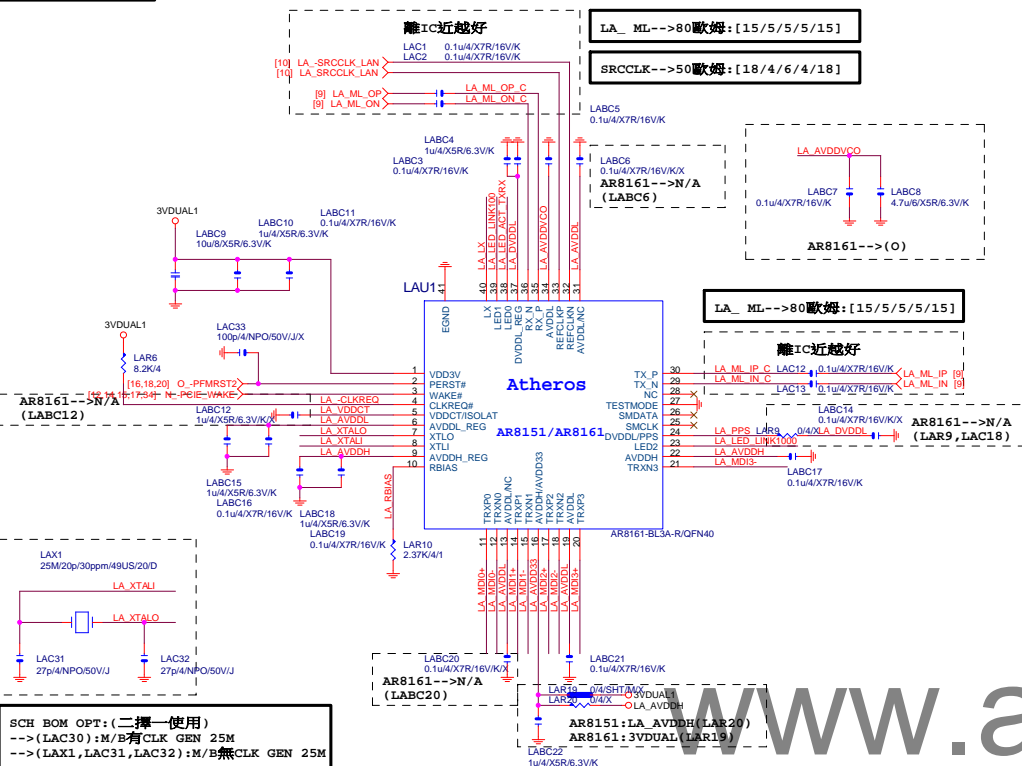
Size	Document Number
Custom	GA-Z77-D3H

Date: Friday, July 13, 2012

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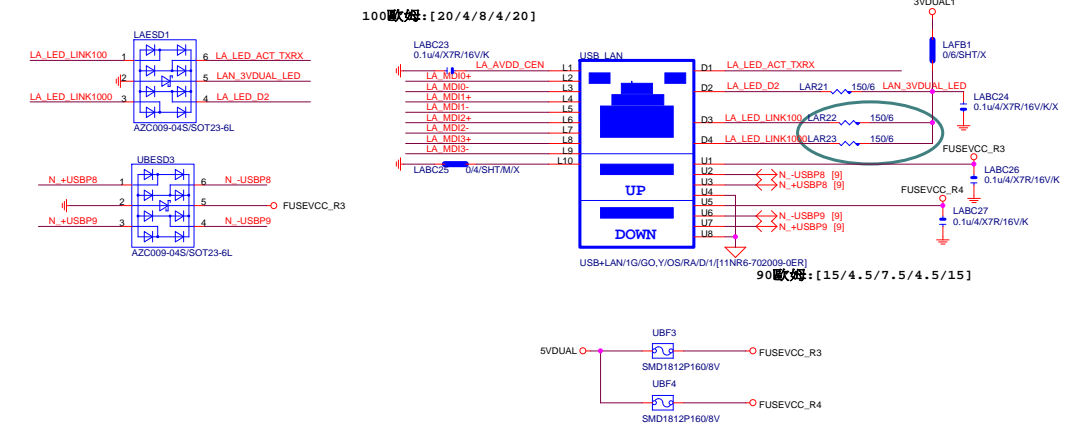
Rev
1.1

LAN:AR8151/AR8161



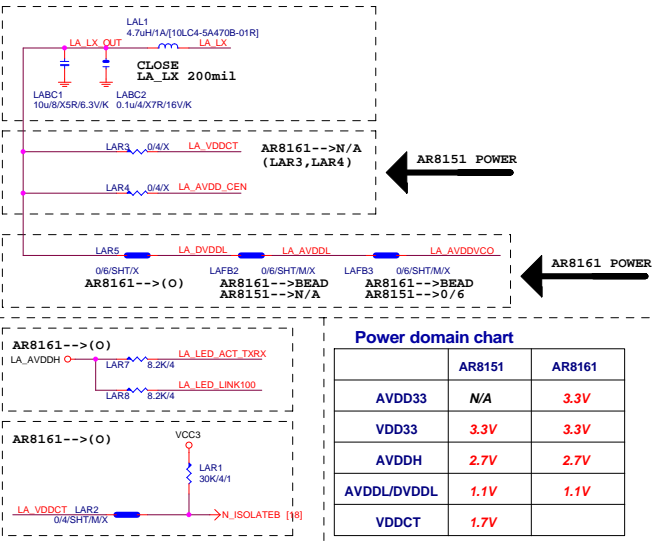
SCH BOM OPT:(二擇一使用)
-->(LAC30):M/B有CLK GEN 25M
-->(LAX1,LAC31,LAC32):M/B無CLK GEN 25M

USB30_LAN CONNECTOR



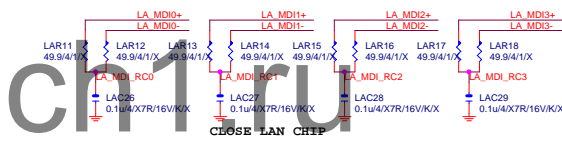
LAN POWER

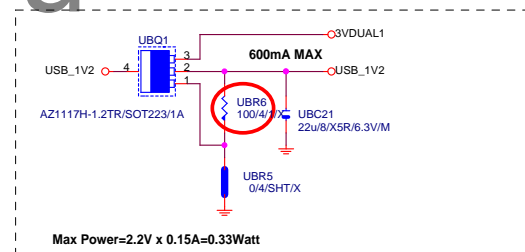
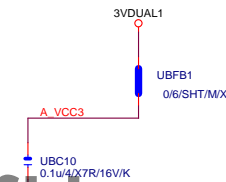
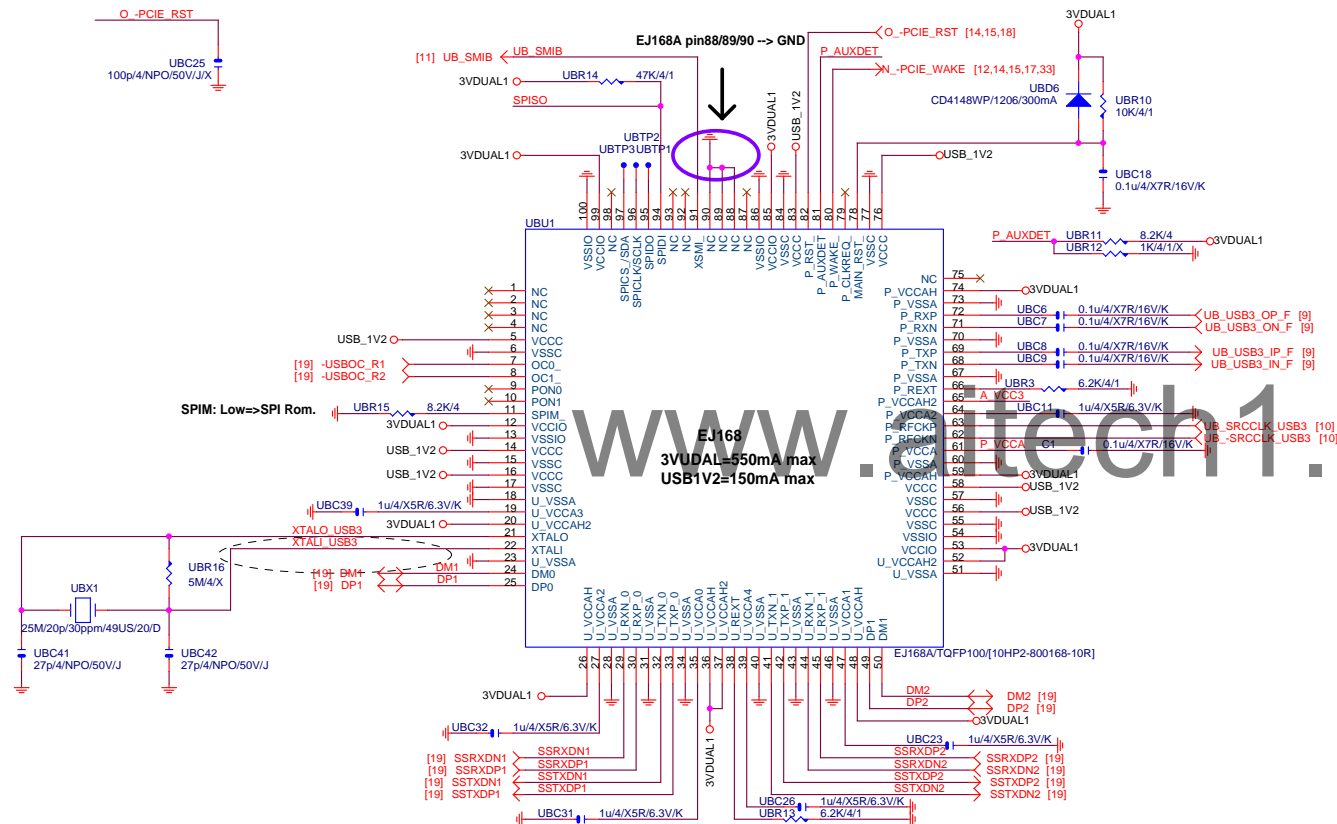
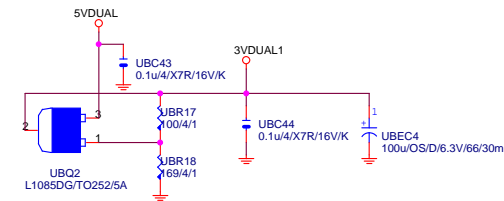
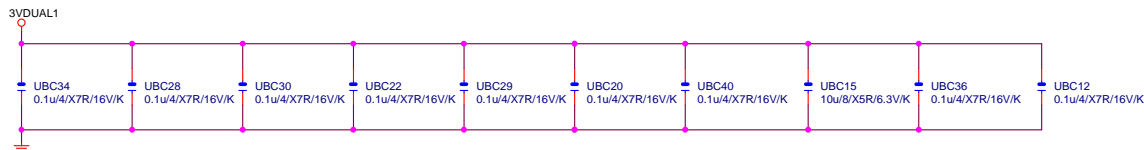
```
NEW DESIGN ONLY FOR INTERNAL SWR
AR8151:LAR3(O),LAR5(X)
AR8161:LAR5(O),LAR3/LAR4(X)
```



	AR8151	AR8161
AVDD33	N/A	3.3V
VDD33	3.3V	3.3V
AVDDH	2.7V	2.7V
AVDDL/DVDDL	1.1V	1.1V
VDDCT	1.7V	

MDI : AR8161-->N/A

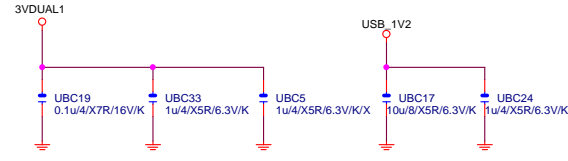
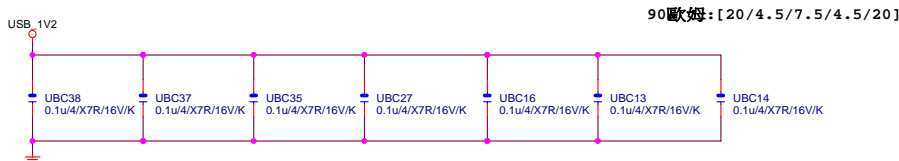




Max Power=2.2V x 0.15A=0.33Watt

AZ1117H-1.2TR/SOT223/1A-->UR17:0/4,UR16:N/A [1.2V]

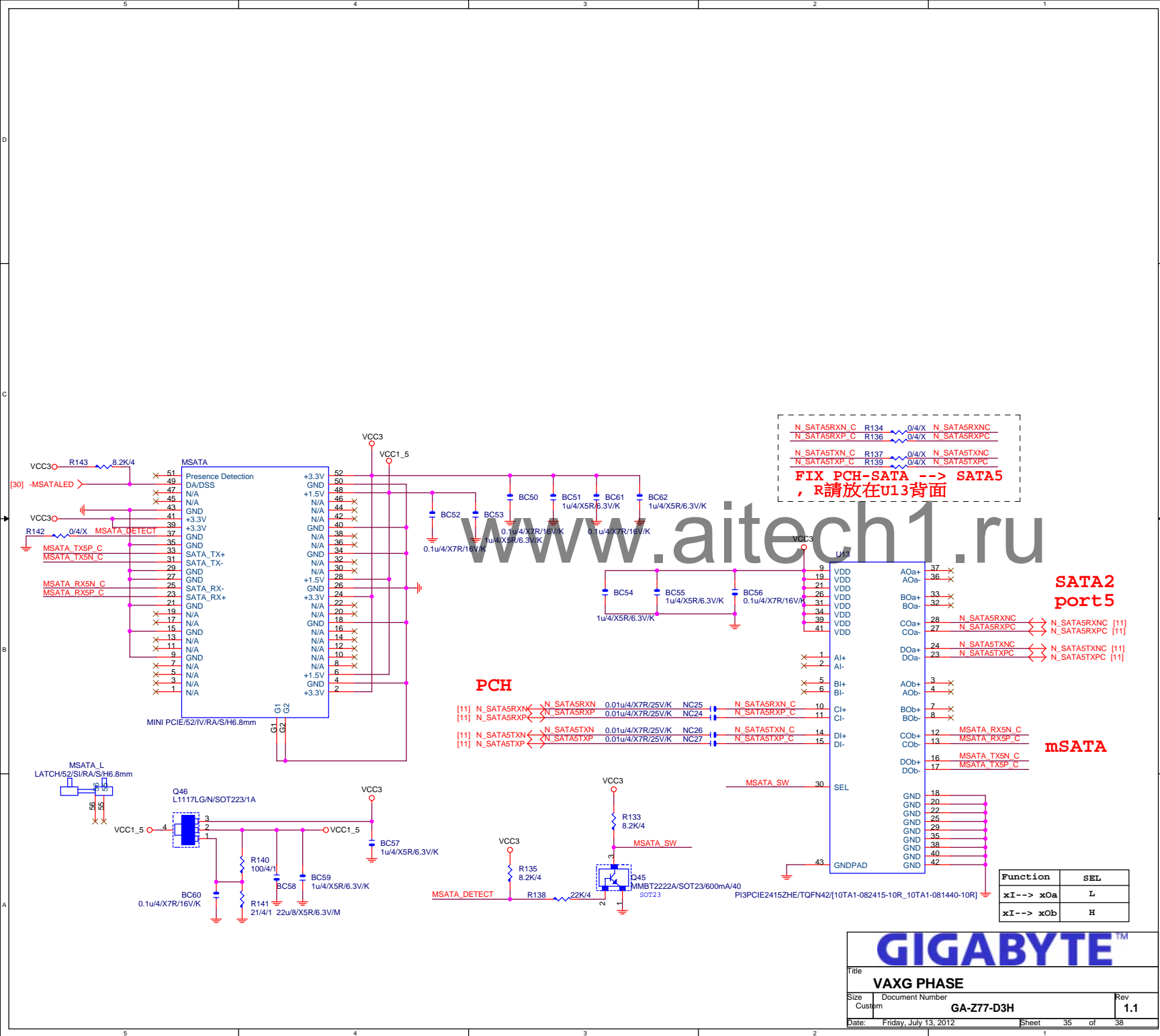
L1117LG/N/SOT223/1A-->UR17:0/4,UR16:100/4/1 [1.25V]

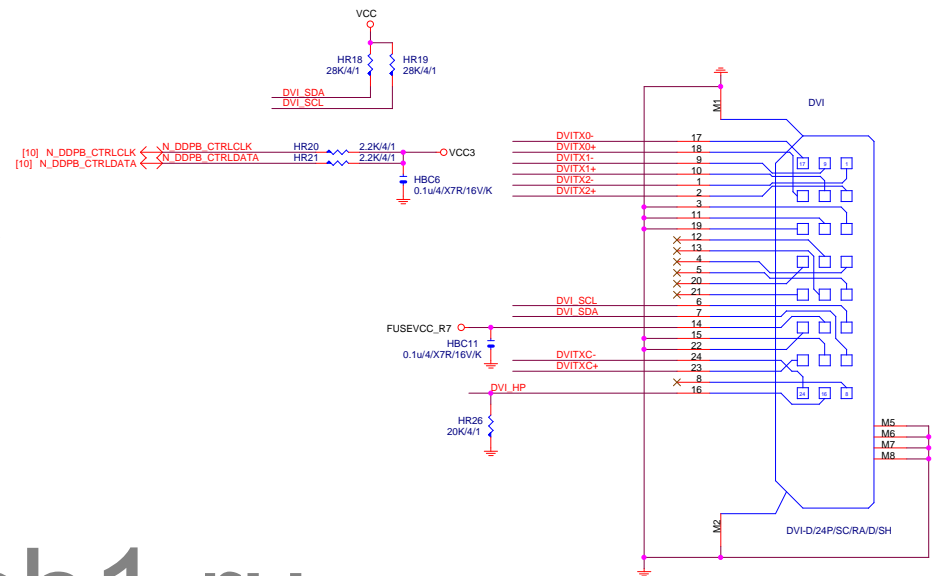
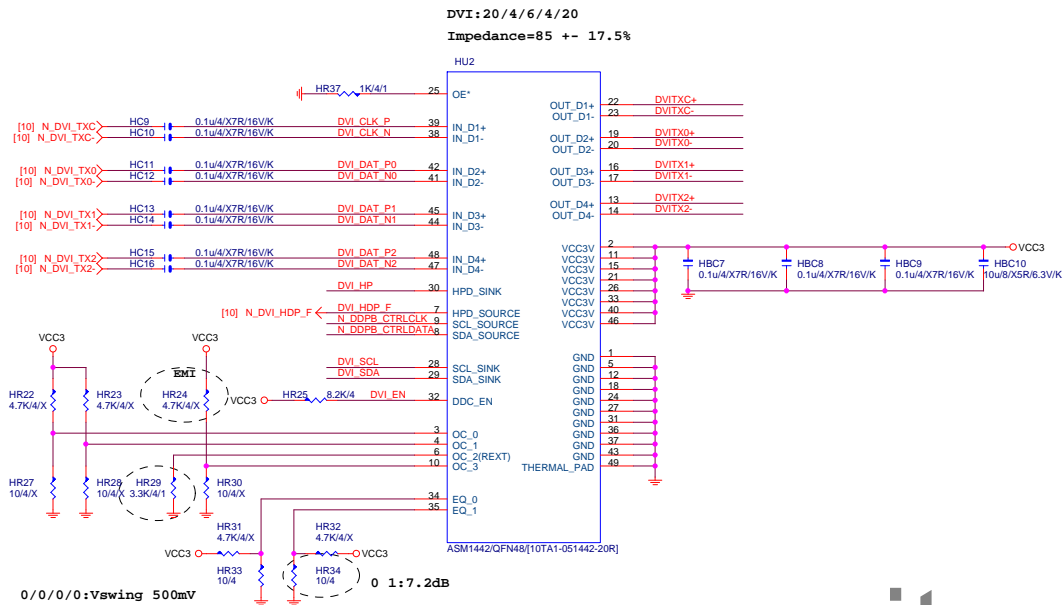


USB3.0 --> 5GHz

BANDWITH=5GHz * (8b/10b)=4Gb/s=500MB/s

GIGABYTE™		
Title E-TRON EJ168		
Size Custom	Document Number GA-Z77-D3H	Rev 1.1
Date: Friday, July 13, 2012	Sheet 34	of 38





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Gigabyte Technology			
Title			
TI TSB43AB23 1394			
Size	Document Number	Rev	
Custom	GA-Z77-D3H	1.1	
Date:	Friday, July 13, 2012	Sheet	36 of 38

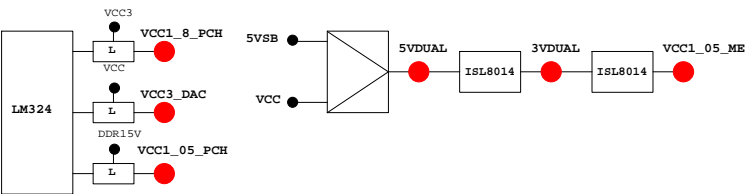


PCH GPIO LIST TABLE					
PIN NAME	PWR	Default	USAG	NOTE	
GP0	MAIN	H-Z	GPI	GPIO0	N/A
GP1/TACH1	MAIN		GPI	GPIO1	N/A
GP2/PIRQE#	MAIN		GPI	-PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN		GPI	-PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN		GPI	-PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN		GPI	-PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN		GPI	PCIEX1 Detect	P/U 8.2K VCC3
GP7/TACH3	MAIN		GPI	GPIO7	P/U 8.2K VCC3
GP8	STBY	H	GPI	GPIO8	N/A
GP9/OC5#	STBY		NATIVE	USB OC5#	N/A
GP10/OC6#	STBY		NATIVE	USB OC6#	N/A
GP11/SMBALERT#	STBY		NATIVE	USB PWR protect	P/U 8.2K 3VDUAL
GP12	STBY	L	GPI	GPIO12	N/A
GP13	STBY	L	GPI	LPCPME#	P/U 8.2K 3VDUAL
GP14/OC7#	STBY		NATIVE	USB OC7#	N/A
GP15	STBY	L	GPI	GPIO15(TLS Enable)	P/U 8.2K 3VDUAL
GP16	MAIN		GPI	GPIO16	P/U 8.2K VCC3
GP17/TACH0	MAIN		GPI	GPIO17	P/U 8.2K VCC3
GP18	MAIN		GPI	Mobile Only	N/A
GP19	MAIN		GPI	GPIO19	P/U 8.2K VCC3
GP20	MAIN		GPI	GPIO20	P/U 8.2K VCC3
GP21	MAIN		GPI	GPIO21	P/U 8.2K VCC3
GP22	MAIN	H-Z	GPI	GPIO22	P/U 8.2K VCC3
GP23	MAIN		GPI	GPIO23	N/A
GP24	STBY	L	GPI	SKTOCC#	N/A
GP25	STBY			Mobile Only	N/A
GP26	STBY			Mobile Only	N/A
GP27	STBY	H	GPO	GPIO27	P/U 8.2K 3VDUAL
GP28	STBY	H	GPO	PWR LED	P/U 8.2K 3VDUAL
GP29	STBY	L	GPI	GPIO29	N/A
GP30	STBY	H-Z	GPI	Mobile Only	N/A
GP31	STBY	H-Z	GPI	Mobile Only	N/A
GP32	MAIN	H	GPO	N/A	N/A
GP33	MAIN	H	GPO	N/A	N/A
GP34	MAIN	H-Z	GPI	-PCI_STOP	P/U 8.2K VCC3
GP35	MAIN	L	GPO	-ACZ_DET	P/U 8.2K VCC3
GP36	MAIN		GPI	N/A	N/A
GP37	MAIN		GPI	N/A	N/A
GP38	MAIN	H-Z	GPI	PCIEX4 Detect	P/U 8.2K VCC3
GP39	MAIN	H-Z	GPI	GPIO39	P/U 8.2K VCC3
GP40	STBY		NATIVE	USB OC1#	N/A
GP41	STBY		NATIVE	USB OC2#	N/A
GP42	STBY		NATIVE	USB OC3#	N/A
GP43	STBY		NATIVE	USB OC4#	N/A
GP44	STBY	L	NATIVE	GPIO44	P/U 8.2K 3VDUAL
GP45	STBY		NATIVE	GPIO45	P/U 8.2K 3VDUAL
GP46	STBY	L	NATIVE	GPIO46	P/U 8.2K 3VDUAL
GP47	STBY			Mobile Only	N/A
GP48	MAIN	H-Z	IN	GPIO48	P/U 8.2K 3VDUAL
GP49	MAIN	H-Z	IN	GPIO49	P/U 8.2K 3VDUAL
GP50	MAIN		NATIVE	-REQ1	P/U 2.2K VCC
GP51	MAIN	H	NATIVE	-GNT1	N/A
GP52	MAIN		NATIVE	-REQ2	P/U 2.2K VCC
GP53	MAIN	H	NATIVE	-GNT2	N/A
GP54	MAIN		NATIVE	-REQ3	P/U 2.2K VCC
GP55	MAIN	H	NATIVE	-GNT3	N/A
GP56	STBY		NATIVE	Mobile Only	N/A
GP57	STBY	H-Z	IN	VCORE_OV1	P/U 8.2K 3VDUAL
GP58	STBY	H-Z	NATIVE	F_USB_OC	P/U 8.2K 3VDUAL
GP59	STBY		NATIVE	USB_OC0#	N/A
GP60	STBY	H-Z	NATIVE	N/A(Reverse)	P/U 8.2K 3VDUAL
GP61	STBY	L	NATIVE	-SUSTAT	N/A
GP62	STBY	L	NATIVE	SUSCLK	N/A
GP63	STBY	L	NATIVE	GPIO63	N/A
GP64	MAIN	L	NATIVE	CLKOUTFLEX0	N/A
GP65	MAIN	L	NATIVE	CLKOUTFLEX1	N/A
GP66	MAIN	L	NATIVE	CLKOUTFLEX2	N/A
GP67	MAIN	L	NATIVE	CLKOUTFLEX3	N/A
GP72	STBY	H-Z	NATIVE	VCORE_OV4	P/U 8.2K 3VDUAL
GP73	STBY			Mobile Only	N/A
GP74	STBY	H-Z	NATIVE	1_05V_OV2	P/U 8.2K 3VDUAL
GP75	STBY	H-Z	NATIVE	N/A(Reverse)	P/U 8.2K 3VDUAL

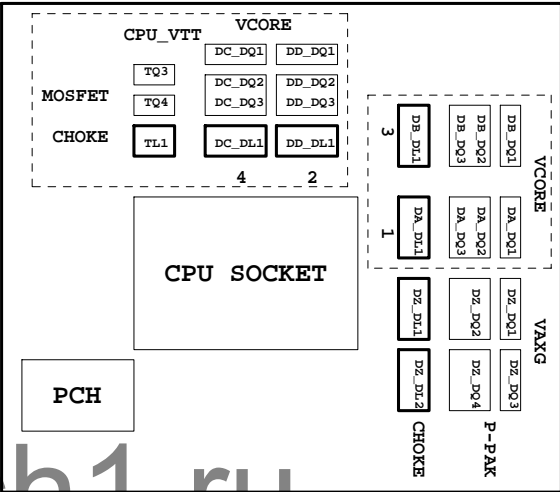
Super I/O ITE8720 GPIO Table

PIN NAME	USAG	NOTE
SVC/PECI_RQT/GP14	-PECI_REQ	
PWROK1/GP13	PWROK1/ITE_PWROK	
KRST#/GP62	-KBRST	
SO/GP50	-ICH_SPI_CS	
IRTX/GP47/CE2_N/JP7	CEB_N	
GP46/IRRX	-LAN2_DSM	
PSION#/GP42	-PSON	
PWROK2#/GP41	PECI_CTL	
PCIRST3#/GP10/VDIMM_STR_EN	-PCIE_RST	
RSMRST#CIRRX1/GP55	-RSMRST	
PME#/GP54	-LPCPME	
PD5/GP75/BUSSO0	N/A	

PIN NAME	USAG	NOTE
FAN_TAC2/GP52	FANIO2	
FAN_TAC3/GP37	FANIO3	
VIDO3/FAN_TAC4/GP25/DSR2#	FANIO4	
FAN_CTL2/GP51	FANPWM2	
FAN_CTL3/GP36	FANPWM3	
VID4/GP34	BEEP-	
VID3/GP33	TURBO1	
VID2/GP32	TURBO0	
VCORE_GOOD/VID6/GP63	CPUT_LED1_C	
VID5/GP35	CPUT_LED2_C	
VID1/GP31	CPUT_LED3_C	
VID0/GP30	-LAN1_DSM	NBT_LED1_C
SLCT/GP80	CPU_LED1_C	
PE/GP81	CPU_LED2_C	
BUSY/GP82	CPU_LED3_C	
PD3/GP73/BUSSI1	SB_LED1_C	
PD4/GP74/BUSSI2	SB_LED2_C	
VCORE_EN/VID7/GP64	IT_GP64	SB_LED3_C
PD0/GP70	NB_LED1_C	
PD1/GP71	NB_LED2_C	
PD2/GP72/BUSSI0	NB_LED3_C	
GP22/SCX	LOW_PWR_1	
VID05/GP27/SIN2	LOW_PWR_2	
PCIRST2#/GP11	-PWRST1	
PCIRST1#/GP12	-PWRST2	
3VBSBW#/GP40	CSI_F0	BSEL166_1
SUSC#/GP53	CSI_F1	BSEL166_2
GP23/SI	BSEL166_3/CSISBSL	
VIDO0/GP20/CTS2#	CPUT_LED1_C	BSEL166_4
GP65/VDDA_EN/GB_01	MB_ID2	
PD6/GP76/BUSSO1	MB_ID3	
PD7/GP77/BUSSO2	MB_ID4	
AFD#/GP86/SMB_C_R	2V PIN	FST_2X8
INIT#/GP85/SMB_D_M	SEC_2x8	GTLREF_AD2
ACK#/GP83	DDR_LED1_C	
VID01/GP21/DCD2#	DDR_LED2_C	
STB#/GP87/SMB_C_M	DDR_LED3_C	
PWRON#GP44	VCORE_OV1	
PANSWH#/GP43	PWRBTSW	
KDAT/GP61	-PWRBTSW	
KCLK/GP60	KDAT	
MDAT/GP57	KCLK	
MACL/GP56	MDAT	
GP66/VLDT_EN/GB_02	NBT_LED1_C	MCLK
SVD/PCIRSTIN#/CIRTX/GP15	PWM2_CR	
KDAT/GP61	PWM2_CR	
GP67/CPU_PG/GB_03	EN_LOADLINE	IT_GP67/-EN_PWM2
SLIN#/GP84/SMB_D_R	-EN_PWM2	
PSI_L/FAN_CLT5/CIRRX2/GP16	-THERM	
VIDO4/GP26/SCOUT2	DDR18V_PH2_EN	
VIDO2/FAN_TAC5/GP24/DSR2#	DDR18V_LED	
VIDO6/GP17/RI2#	1_1V_PH_EN	
VIDO7/JP6/DTR2#	JP6	
PD5/GP75/BUSSO0	SB_LED3_C	



PWM各相位的擺法如下：



BIOS超電壓對應表：

線路圖名稱	BIOS選項
Vcore	CPU Vcore
CPU_VTT	CPU Termination
CPU_VAXG	CPU Graphic Core
VCC1_8_PCH	CPU PLL
VCC1_05_PCH	PCH core
3VDUAL	3VDUAL
DDR15V	DRAM voltage
DDRVTT	DRAM Terminatio
VREF_CA_A/VREF_CA_B	DRAM Address Ref
VREF_DQ_A/VREF_DQ_B	DRAM Data Ref

散熱模組料號：

Z77-D3H :
PCH :
12SP2-S05511-01R/02R/03R
MOSFET :
12SP2-S08924-01R/02R/03R

	3 pin FAN control	4 pin FAN control	FAN speed	Controller
CPU FAN	FANPWM1	FANPWM3	FANIO1	IT8720
	ICH_FAN_PWM2	ICH_FAN_PWM0	ICH_FAN_TACH0	PCH
SYS FAN	FANPWM2	N/A	FANIO2	IT8720
	ICH_FAN_PWM1	N/A	ICH_FAN_TACH1	PCH
PWR FAN	N/A	N/A	FANIO3	IT8720
			ICH_FAN_TACH2	PCH